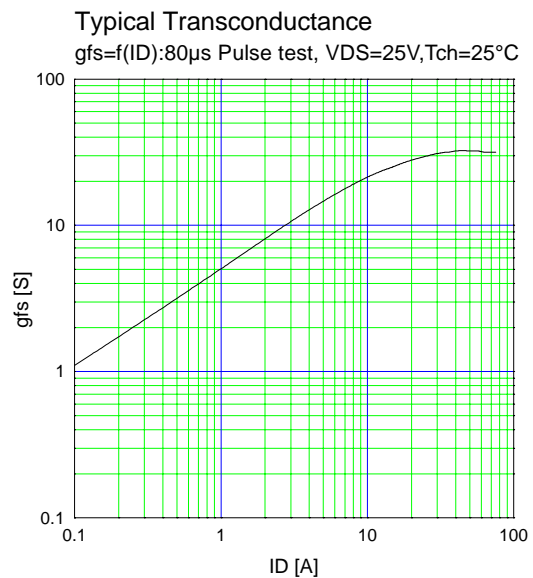
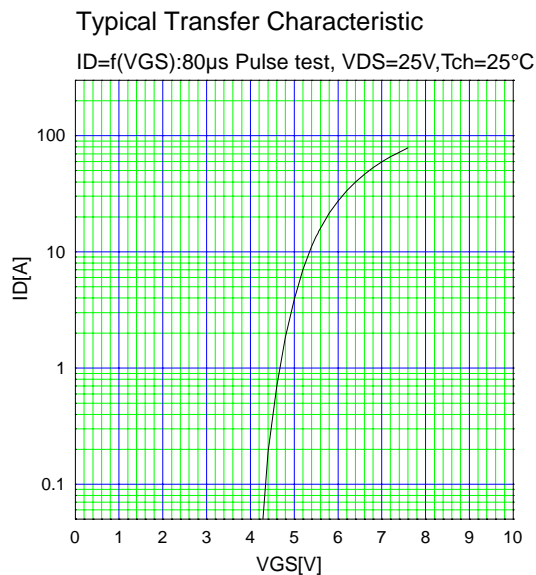
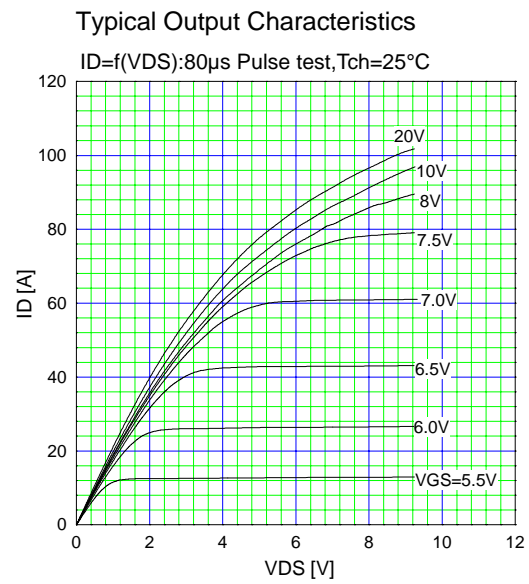
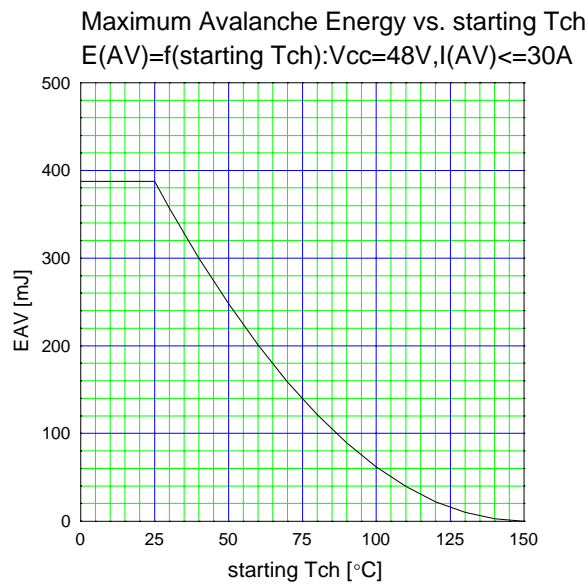
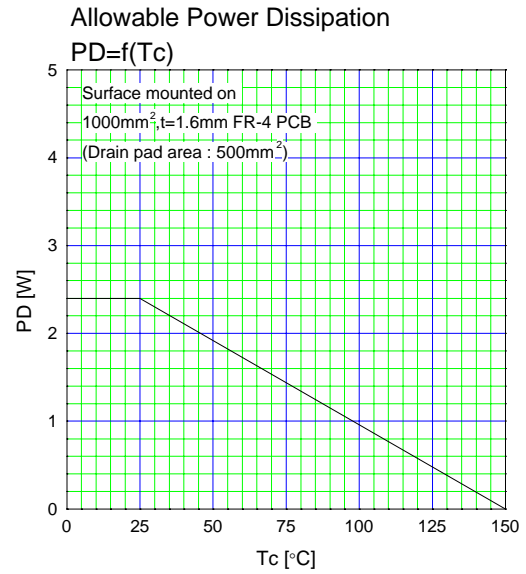
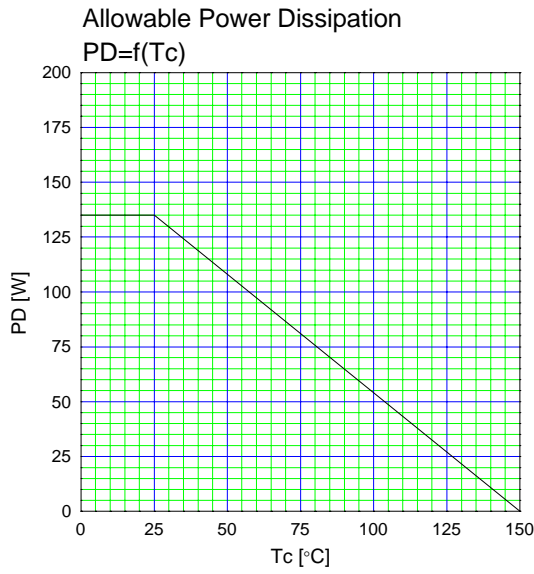
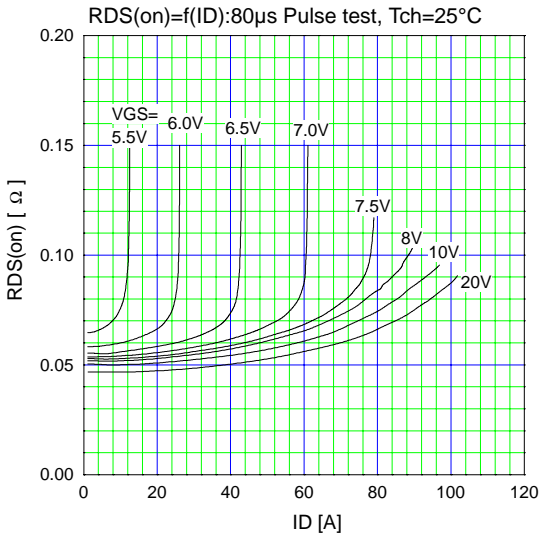


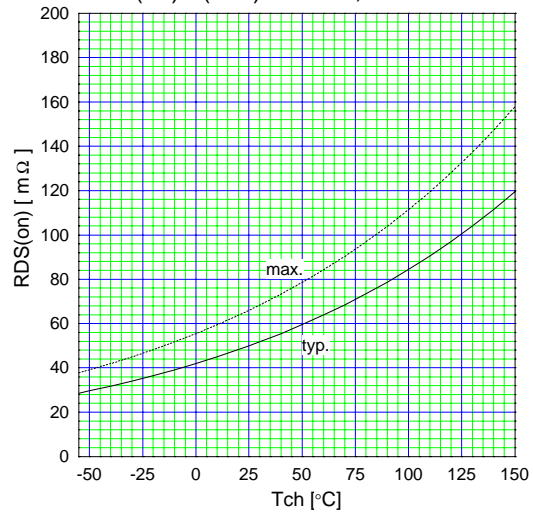
Characteristics



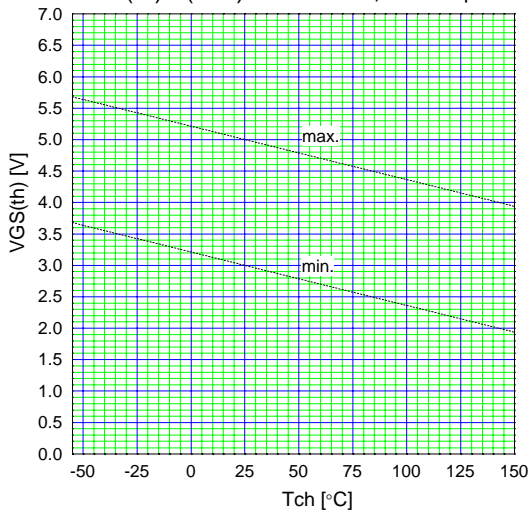
Typical Drain-Source on-state Resistance



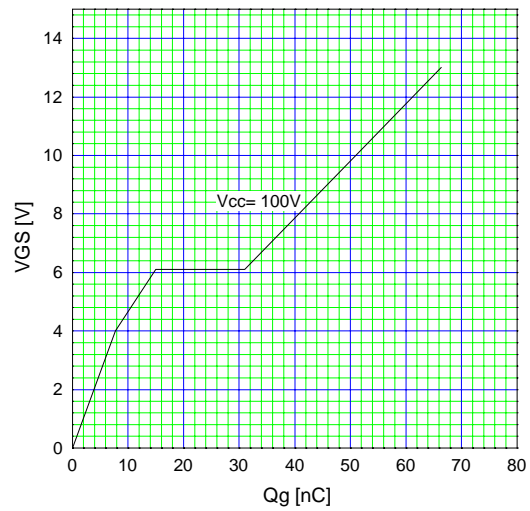
Drain-Source On-state Resistance
 $R_{DS(on)}=f(T_{ch})$: $I_D=15\text{A}$, $V_{GS}=10\text{V}$



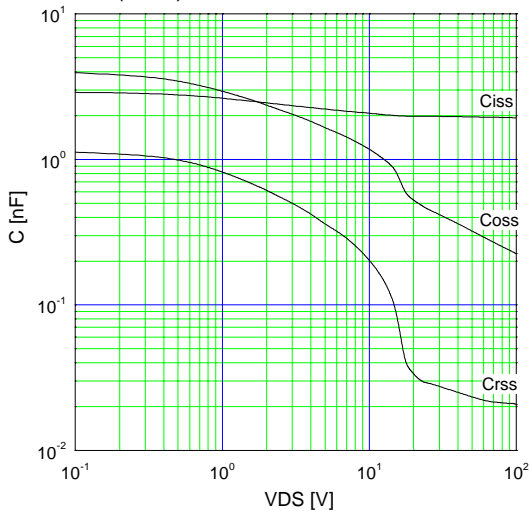
Gate Threshold Voltage vs. T_{ch}
 $V_{GS(th)}=f(T_{ch})$: $V_{DS}=V_{GS}$, $I_D=250\mu\text{A}$



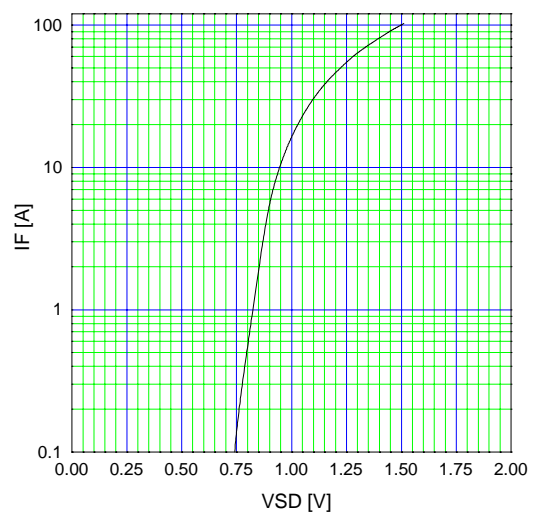
Typical Gate Charge Characteristics
 $V_{GS}=f(Q_g)$: $I_D=30\text{A}$, $T_{ch}=25^\circ\text{C}$



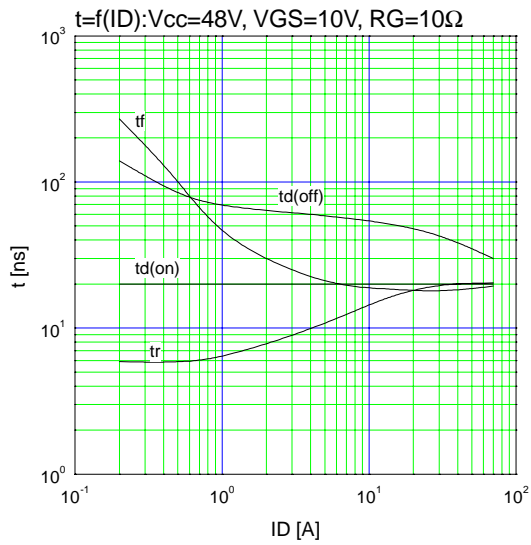
Typical Capacitance
 $C=f(V_{DS})$: $V_{GS}=0\text{V}$, $f=1\text{MHz}$



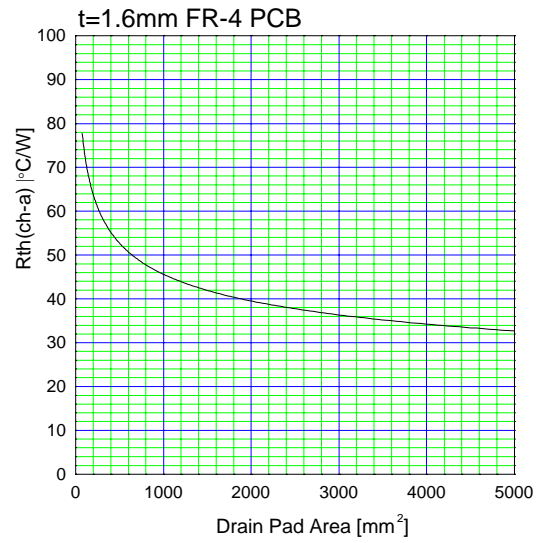
Typical Forward Characteristics of Reverse Diode
 $I_F=f(V_{SD})$: 80 μ s Pulse test, $T_{ch}=25^\circ\text{C}$



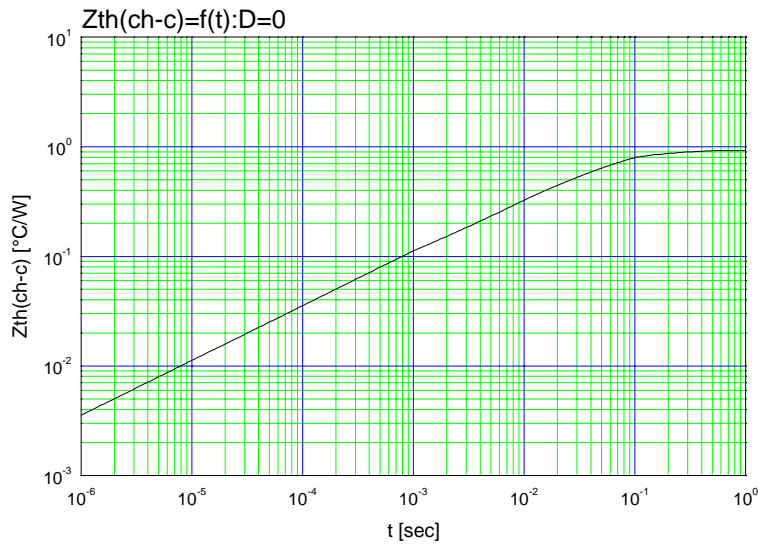
Typical Switching Characteristics vs. ID



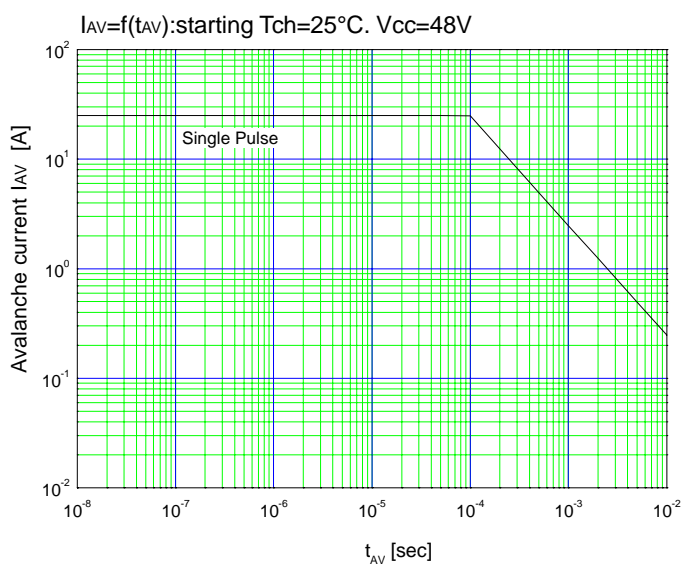
Thermal Resistance vs. Drain Pad area



Transient Thermal Impedance



Maximum Avalanche Current Pulsewidth



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