



2SK3283

N-Channel Silicon MOSFET

Load Switching Applications

Features

- Low ON-resistance.
- 4V drive.

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V _{DSS}		30	V
Gate-to-Source Voltage	V _{GSS}		±20	V
Drain Current (DC)	I _D		15	A
Drain Current (Pulse)	I _{DP}	PW≤10μs, duty cycle≤1%	45	A
Allowable Power Dissipation	P _D		1	W
		T _c =25°C	20	W
Channel Temperature	T _{ch}		150	°C
Storage Temperature	T _{stg}		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	I _D =1mA, V _{GS} =0	30			V
Zero-Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V, V _{GS} =0			1	μA
Gate-to-Source Leakage Current	I _{GSS}	V _{GS} =±16V, V _{DS} =0			±10	μA
Cutoff Voltage	V _{GS(off)}	V _{DS} =10V, I _D =1mA	1.0		2.4	V
Forward Transfer Admittance	y _{fs}	V _{DS} =10V, I _D =10A	11.2	16		S
Static Drain-to-Source On-State Resistance	R _{DS(on) 1}	I _D =10A, V _{GS} =10V		17	23	mΩ
	R _{DS(on) 2}	I _D =4A, V _{GS} =4V		25	35	mΩ
Input Capacitance	C _{iss}	V _{DS} =10V, f=1MHz		1550		pF
Output Capacitance	C _{oss}	V _{DS} =10V, f=1MHz		350		pF
Reverse Transfer Capacitance	C _{rss}	V _{DS} =10V, f=1MHz		220		pF

Marking : K3283

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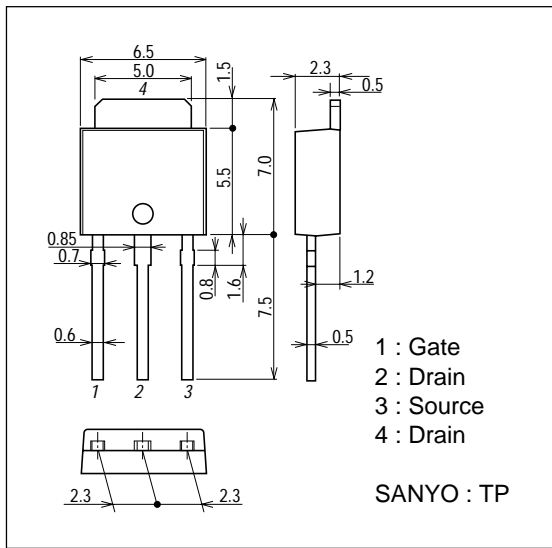
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Turn-ON Delay Time	$t_{d(on)}$	See specified Test Circuit.		12		ns
Rise Time	t_r	See specified Test Circuit.		300		ns
Turn-OFF Delay Time	$t_{d(off)}$	See specified Test Circuit.		105		ns
Fall Time	t_f	See specified Test Circuit.		100		ns
Total Gate Charge	Q_g	$V_{DS}=10V, V_{GS}=10V, I_D=15A$		40		nC
Gate-to-Source Charge	Q_{gs}	$V_{DS}=10V, V_{GS}=10V, I_D=15A$		5		nC
Gate-to-Drain "Miller" Charge	Q_{gd}	$V_{DS}=10V, V_{GS}=10V, I_D=15A$		7		nC
Diode Forward Voltage	V_{SD}	$I_S=15A, V_{GS}=0$	0.92	1.2		V

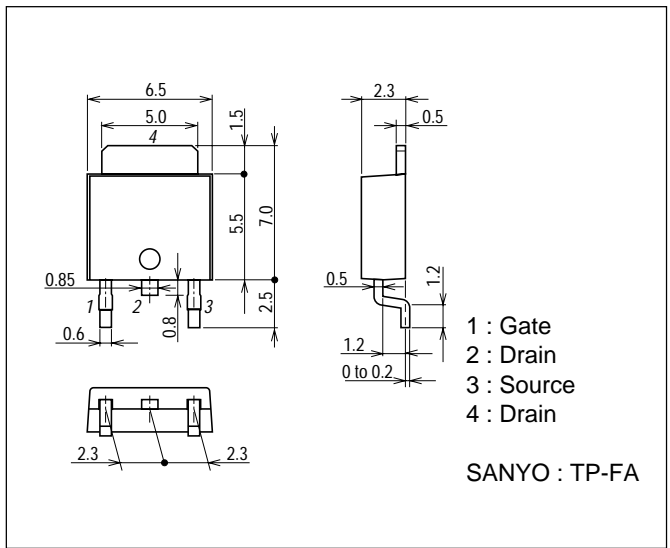
Package Dimensions

unit : mm
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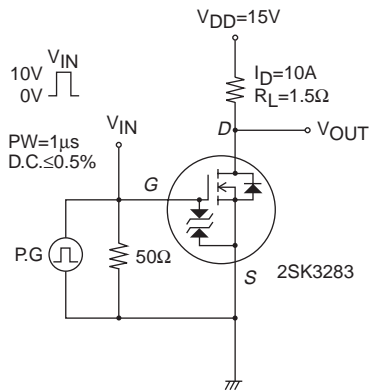


Package Dimensions

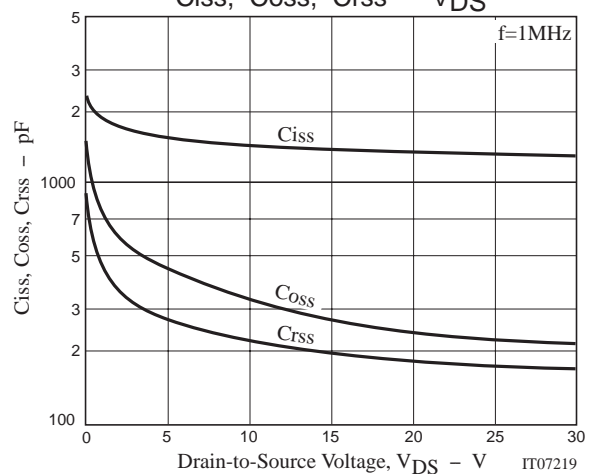
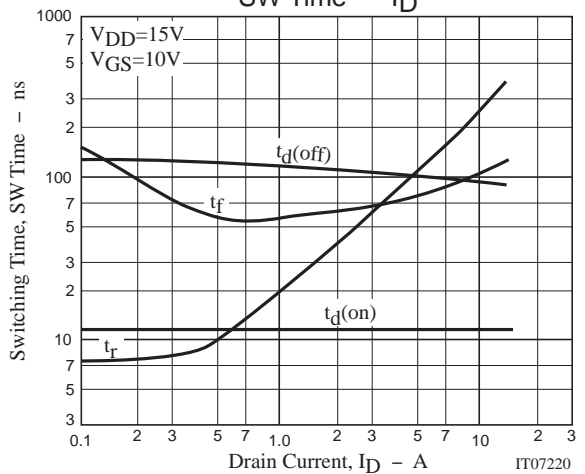
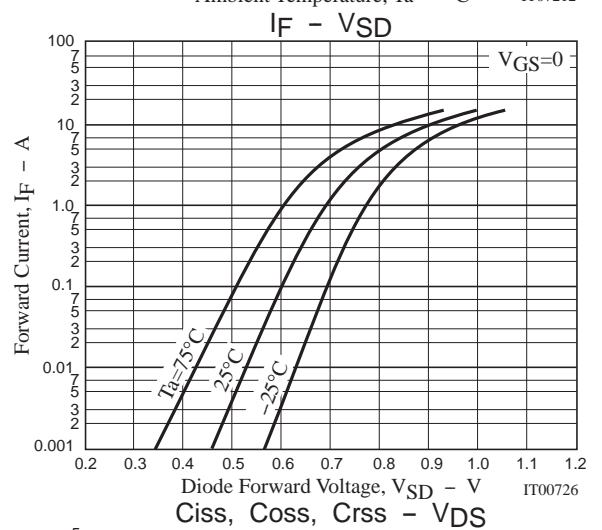
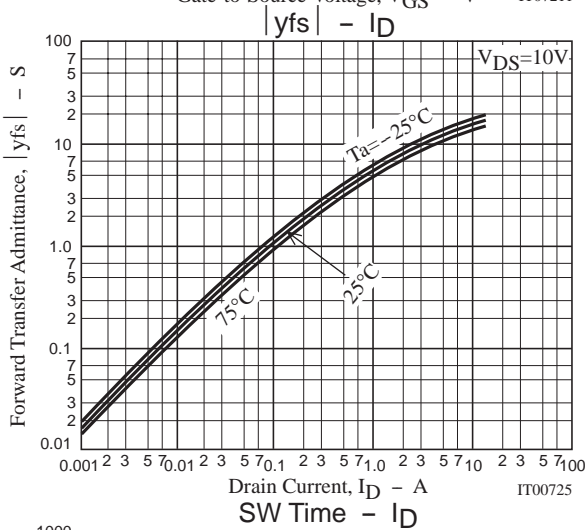
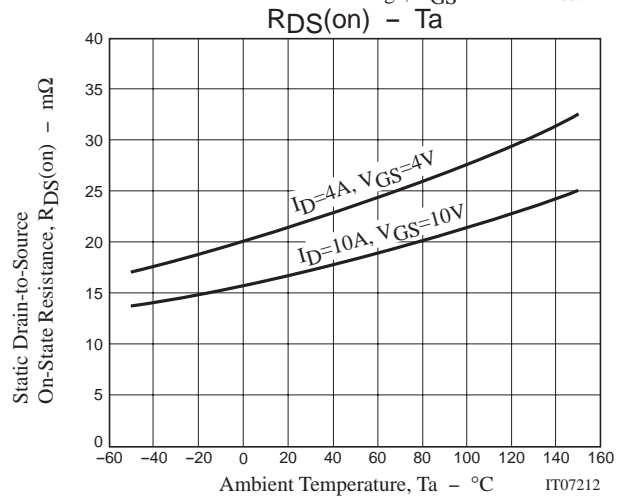
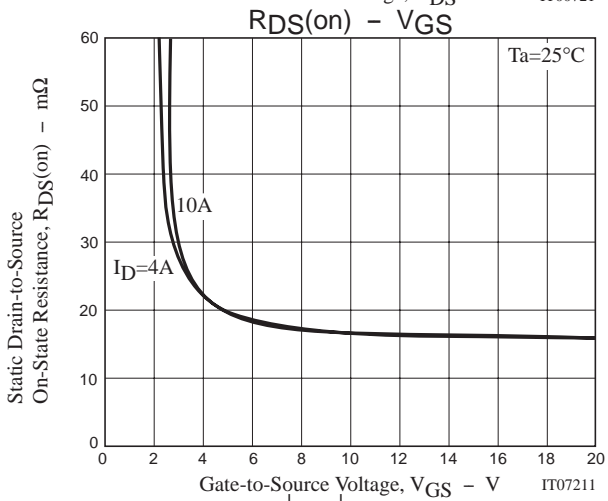
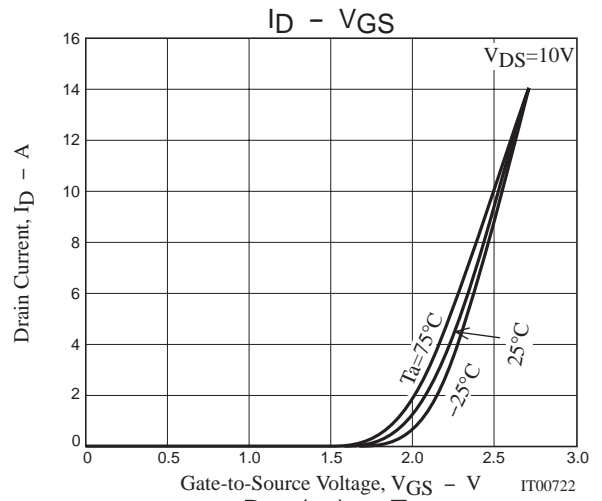
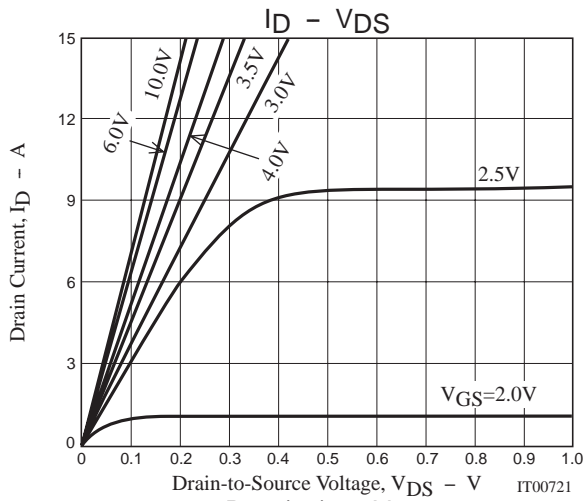
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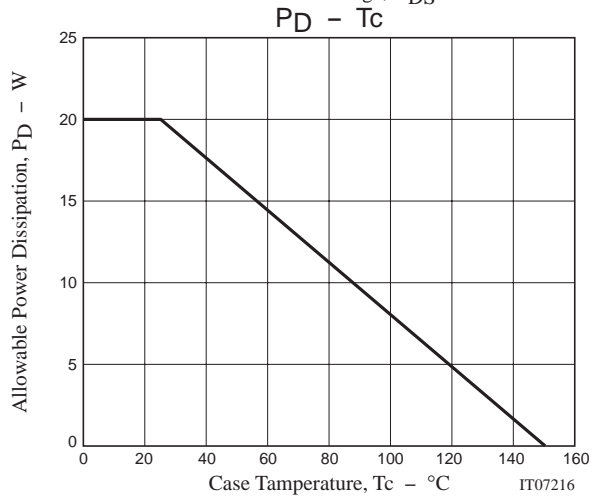
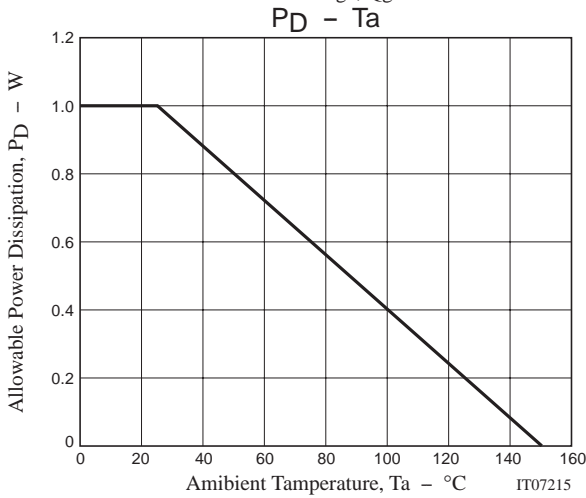
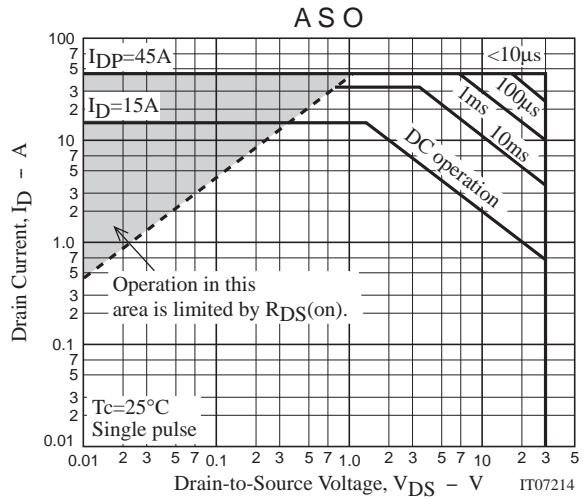
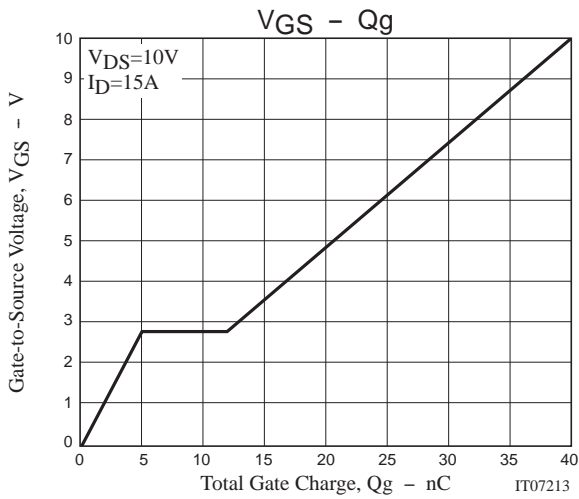


Switching Time Test Circuit



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