

## N-CHANNEL MOS FET FOR SWITCHING

### DESCRIPTION

The 2SK1580 is an N-channel vertical type MOS FET which can be driven by 2.5 V power supply.

As the MOS FET is driven by low voltage and does not require consideration of driving current, it is suitable for appliance including VCR cameras and headphone stereos which need power saving.

### FEATURES

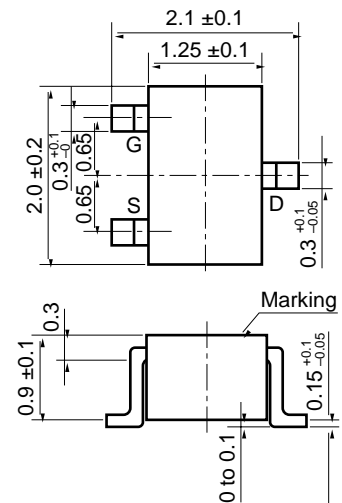
- Directly driven by ICs having a 3 V power supply.
- Not necessary to consider driving current because of its high input impedance.
- Possible to reduce the number of parts by omitting the bias resistor.

### ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub> = 25°C)

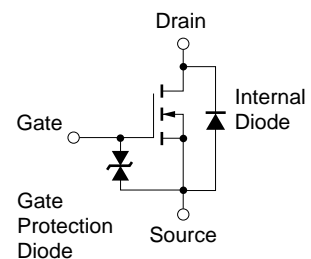
Drain to Source Voltage (V <sub>GS</sub> = 0 V)	V <sub>DSS</sub>	16	V
Gate to Source Voltage (V <sub>DS</sub> = 0 V)	V <sub>GSS</sub>	±16	V
Drain Current (DC) (T <sub>C</sub> = 25°C)	I <sub>D(DC)</sub>	±100	mA
Drain Current (pulse) <sup>Note1</sup>	I <sub>D(pulse)</sub>	±200	mA
Total Power Dissipation (T <sub>A</sub> = 25°C)	P <sub>T</sub>	150	mW
Channel Temperature	T <sub>ch</sub>	150	°C
Operating Temperature	T <sub>opt</sub>	-55 to +80	°C
Storage Temperature	T <sub>stg</sub>	-55 to +150	°C

**Note1.** PW ≤ 10 ms, Duty Cycle ≤ 50%

### PACKAGE DRAWING (Unit : mm)



### EQUIVALENT CIRCUIT



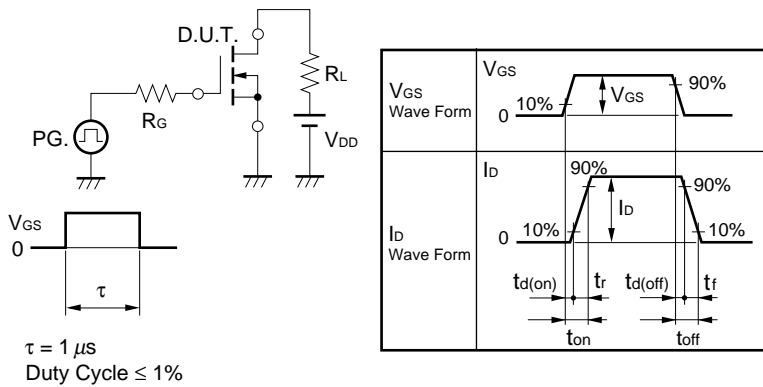
**Remark** The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device is actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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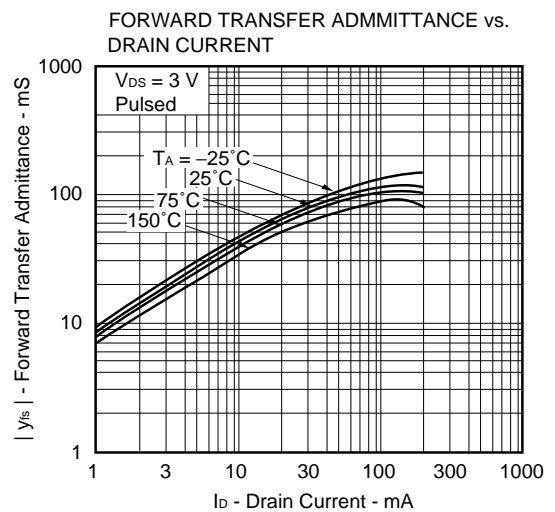
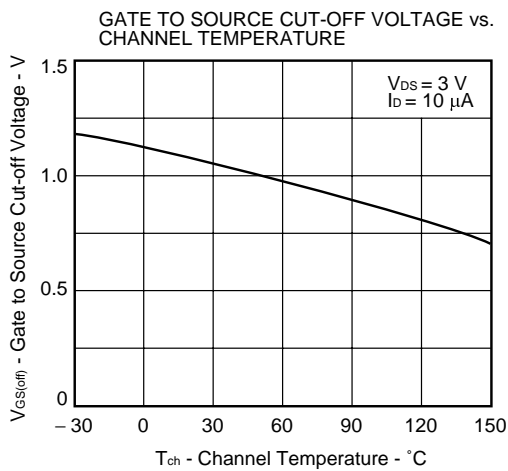
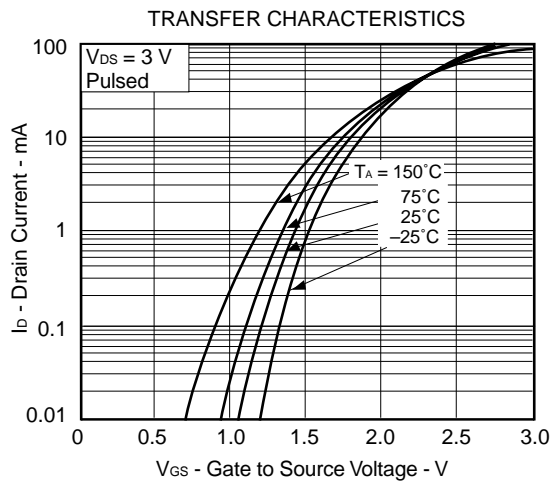
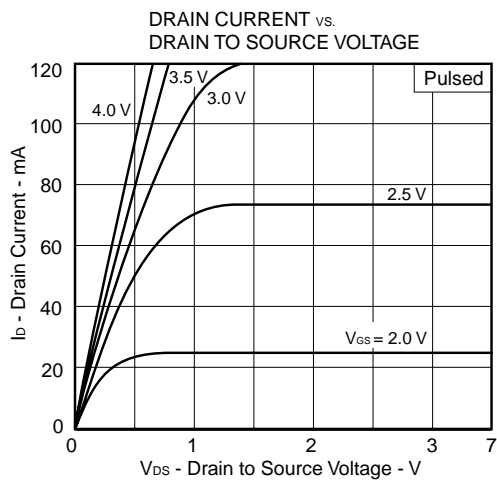
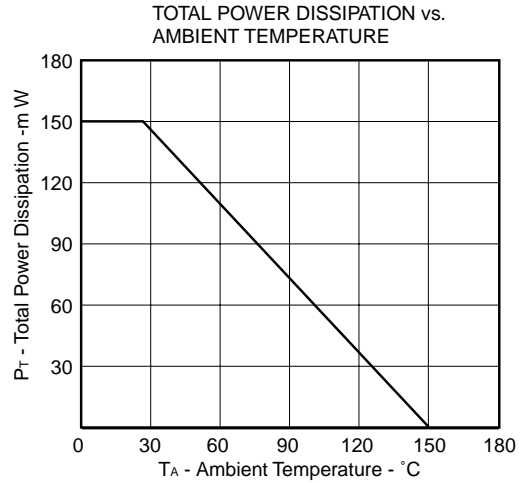
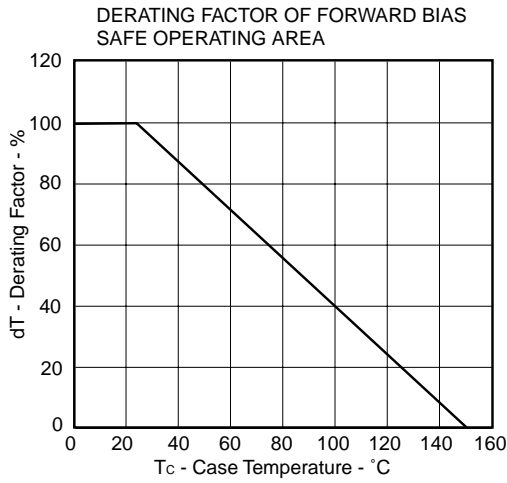
★ ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C)

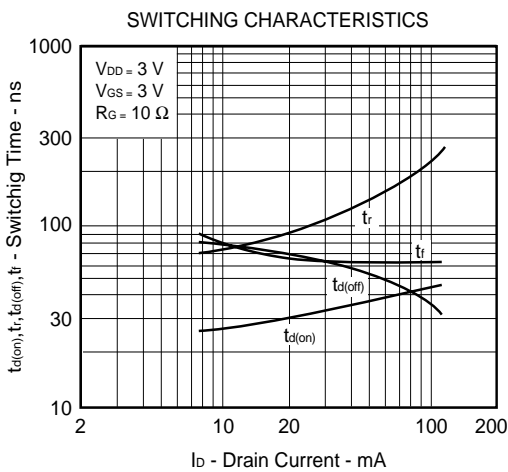
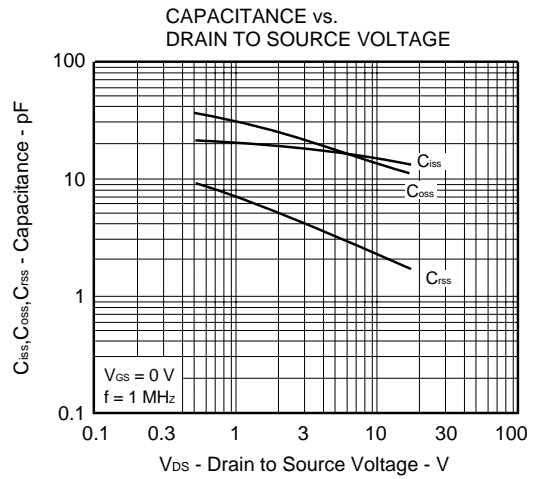
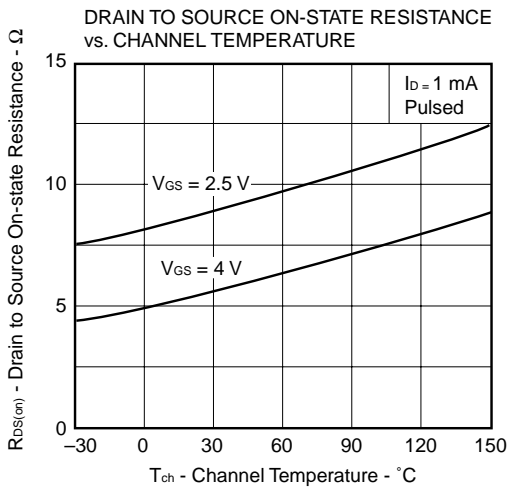
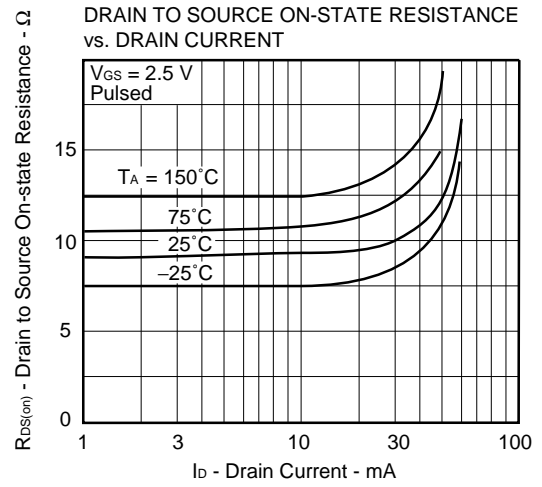
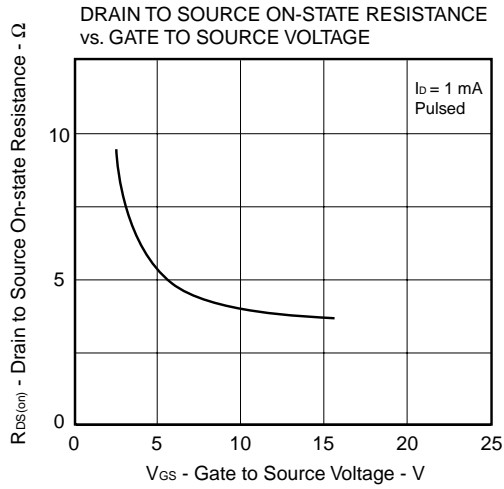
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 16 V, V <sub>GS</sub> = 0 V			1.0	μA
Gate Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = ±3.0 V, V <sub>DS</sub> = 0 V			±5.0	nA
Gate Cut-off Voltage	V <sub>GS(off)</sub>	V <sub>DS</sub> = 3 V, I <sub>D</sub> = 10 μA	0.8	1.1	1.6	V
Forward Transfer Admittance	y <sub>fs</sub>	V <sub>DS</sub> = 3 V, I <sub>D</sub> = 10 mA	20	44		mS
Drain to Source On-state Resistance	R <sub>DS(on)1</sub>	V <sub>GS</sub> = 2.5 V, I <sub>D</sub> = 1 mA		9	15	Ω
	R <sub>DS(on)2</sub>	V <sub>GS</sub> = 4.0 V, I <sub>D</sub> = 1 mA		6	10	Ω
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 3 V		18		pF
Output Capacitance	C <sub>oss</sub>	V <sub>GS</sub> = 0 V		22		pF
Reverse Transfer Capacitance	C <sub>rss</sub>	f = 1 MHz		4		pF
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = 3 V, I <sub>D</sub> = 10 mA		27		ns
Rise Time	t <sub>r</sub>	V <sub>GS</sub> = 3 V		75		ns
Turn-off Delay Time	t <sub>d(off)</sub>	R <sub>G</sub> = 10 Ω		78		ns
Fall Time	t <sub>f</sub>	R <sub>L</sub> = 300 Ω		80		ns

TEST CIRCUIT SWITCHING TIME



TYPICAL CHARACTERISTICS (T<sub>A</sub> = 25°C)





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