

# MOS FIELD EFFECT TRANSISTOR

# 2SK1485

## N-CHANNEL MOS FIELD EFFECT TRANSISTOR FOR SWITCHING

### DESCRIPTION

The 2SK1485, N-channel vertical type MOS FET is a switching device which can be driven directly by the output of ICs having a 5 V power source.

As the MOS FET has low on-state resistance and excellent switching characteristics, it is suitable for driving actuators such as motors, relays, and solenoids.

### FEATURES

- Directly driven by ICs having a 5 V power source.
- Low on-state resistance  
 $R_{DS(on)1} = 1.2 \Omega \text{ MAX. (} V_{GS} = 4.0 \text{ V, } I_D = 0.5 \text{ A)}$   
 $R_{DS(on)2} = 0.8 \Omega \text{ MAX. (} V_{GS} = 10 \text{ V, } I_D = 0.5 \text{ A)}$
- Complementary to 2SJ199.

### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ )

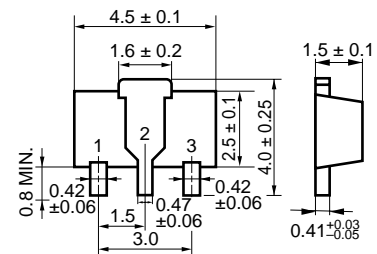
Drain to Source Voltage ( $V_{GS} = 0 \text{ V}$ )	$V_{DSS}$	100	V
Gate to Source Voltage ( $V_{DS} = 0 \text{ V}$ )	$V_{GSS}$	$\pm 20$	V
★ Drain Current (DC) ( $T_c = 25^\circ\text{C}$ )	$I_{D(DC)}$	$\pm 1.0$	A
Drain Current (pulse) <sup>Note1</sup>	$I_{D(pulse)}$	$\pm 2.0$	A
Total Power Dissipation ( $T_A = 25^\circ\text{C}$ ) <sup>Note2</sup>	$P_T$	2.0	W
Channel Temperature	$T_{ch}$	150	$^\circ\text{C}$
Storage Temperature	$T_{stg}$	-55 to +150	$^\circ\text{C}$

**Notes1.**  $PW \leq 10 \text{ ms}$ , Duty Cycle  $\leq 50\%$

**2.** Mounted on ceramic board of  $16 \text{ cm}^2 \times 0.7 \text{ mm}$

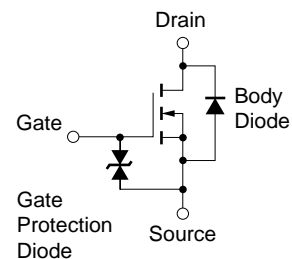
**Remark** The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

### PACKAGE DRAWING (Unit : mm)



1.Source  
2.Drain  
3.Gate  
MARK : NC

### EQUIVALENT CIRCUIT

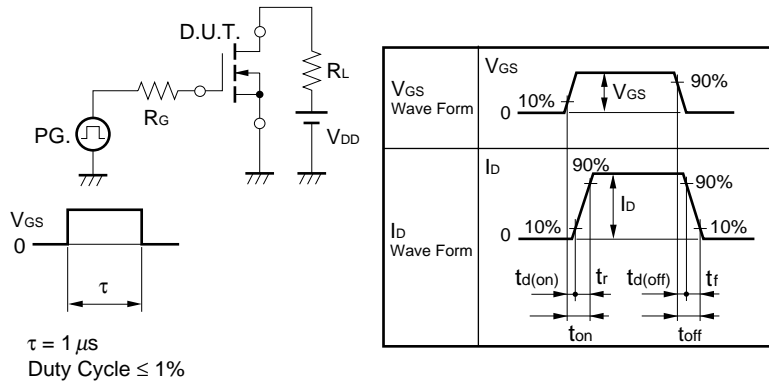


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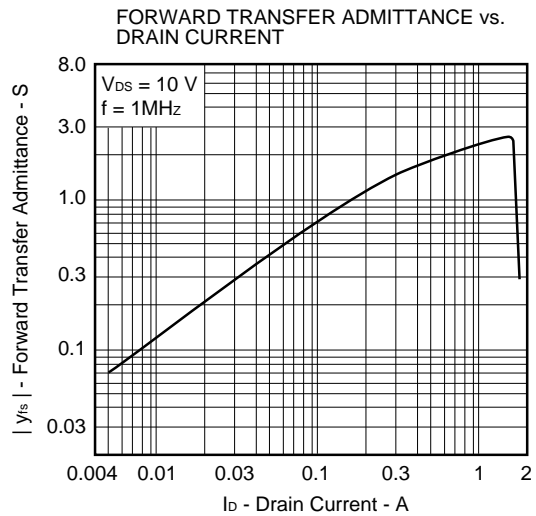
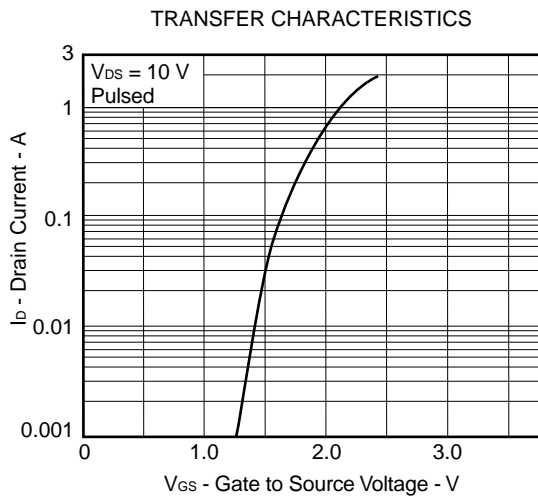
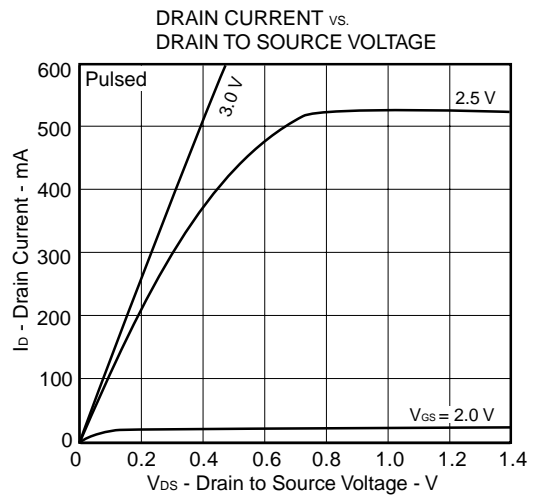
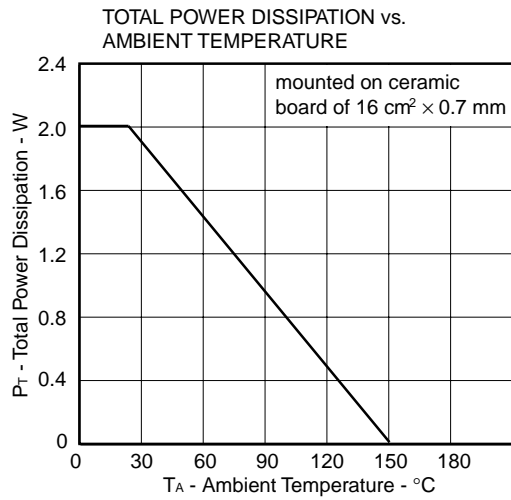
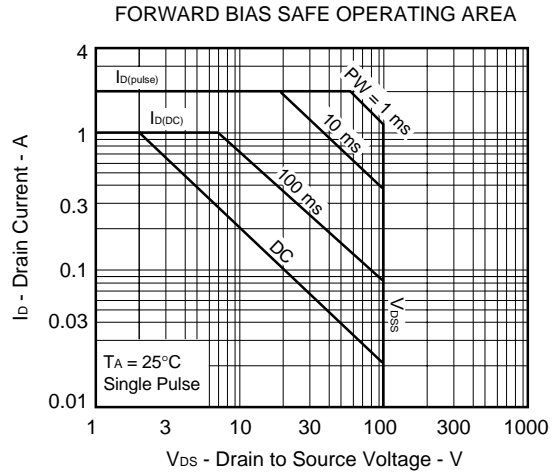
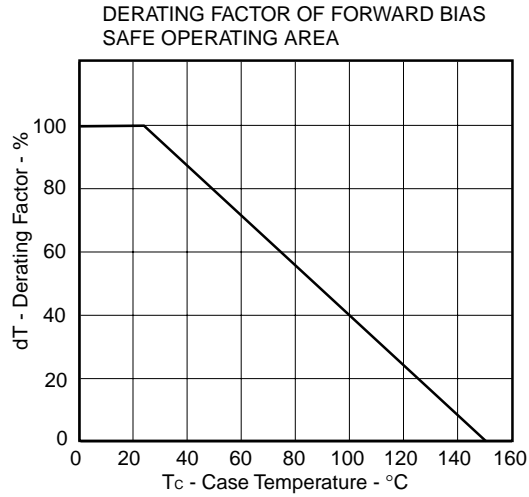
**ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C)**

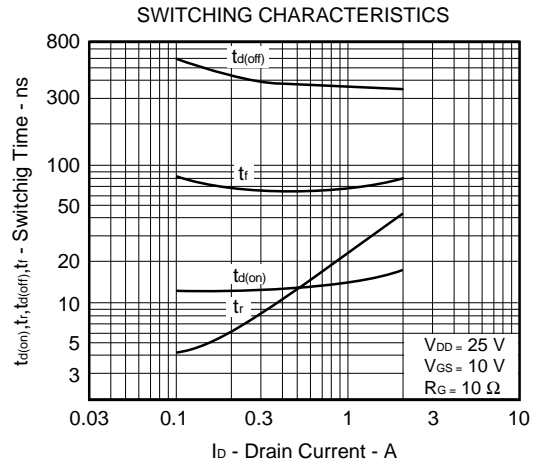
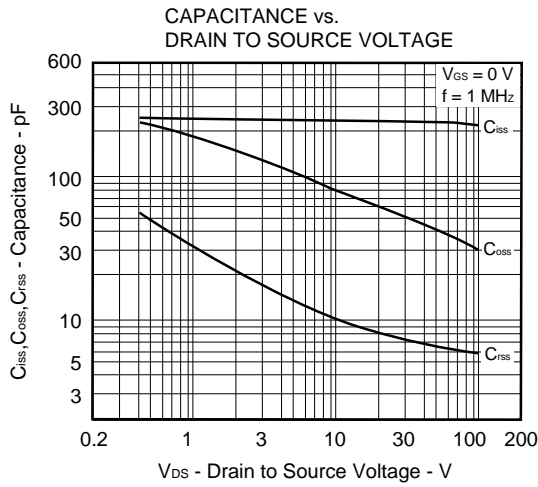
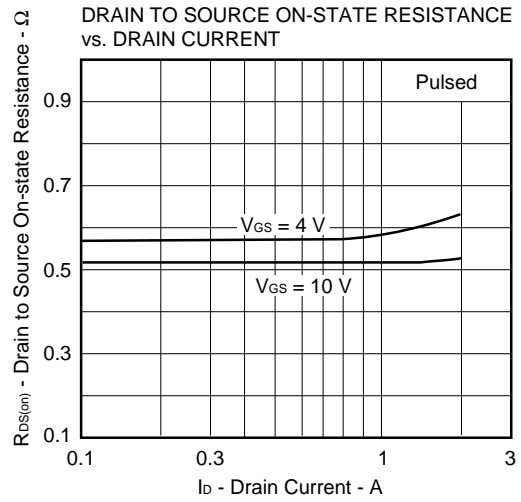
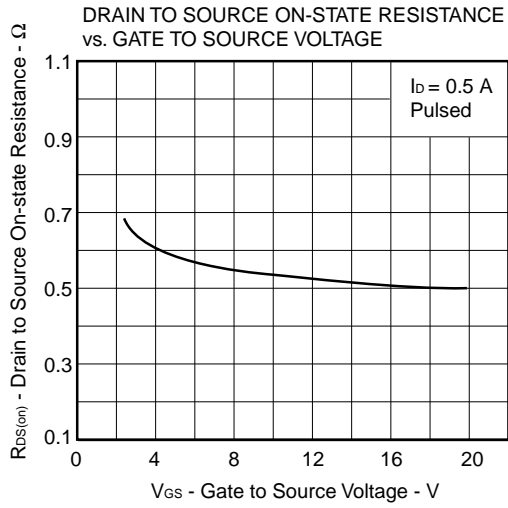
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V			10	μA
Gate Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = ±20 V, V <sub>DS</sub> = 0 V			±10	μA
Gate Cut-off Voltage	V <sub>GS(off)</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	0.8	1.2	2.0	V
Forward Transfer Admittance	y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 0.5 A	0.4			S
Drain to Source On-state Resistance	R <sub>DS(on)1</sub>	V <sub>GS</sub> = 4.0 V, I <sub>D</sub> = 0.5 A		0.6	1.2	Ω
	R <sub>DS(on)2</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 0.5 A		0.5	0.8	Ω
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 10 V		230		pF
Output Capacitance	C <sub>OSS</sub>	V <sub>GS</sub> = 0 V		80		pF
Reverse Transfer Capacitance	C <sub>rss</sub>	f = 1 MHz		12		pF
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = 25 V, I <sub>D</sub> = 0.5 A		14		ns
Rise Time	t <sub>r</sub>	V <sub>GS</sub> = 10 V		14		ns
Turn-off Delay Time	t <sub>d(off)</sub>	R <sub>G</sub> = 10 Ω		370		ns
Fall Time	t <sub>f</sub>			65		ns

**SWITCHING TIME**

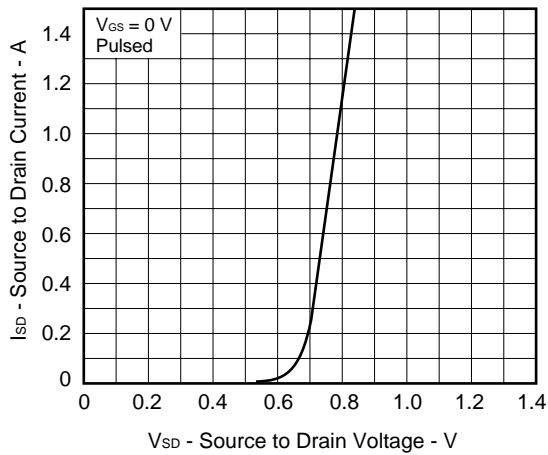


TYPICAL CHARACTERISTICS (T<sub>A</sub> = 25°C)





**SOURCE TO DRAIN DIODE FORWARD VOLTAGE**



[MEMO]

[MEMO]

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