

# 2N3906

Preferred Device

## General Purpose Transistors

### PNP Silicon

#### Features

- Pb-Free Packages are Available\*

#### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	$V_{CEO}$	40	Vdc
Collector – Base Voltage	$V_{CBO}$	40	Vdc
Emitter – Base Voltage	$V_{EBO}$	5.0	Vdc
Collector Current – Continuous	$I_C$	200	mAdc
Total Device Dissipation @ $T_A = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	$P_D$	625 5.0	mW mW/ $^\circ\text{C}$
Total Power Dissipation @ $T_A = 60^\circ\text{C}$	$P_D$	250	mW
Total Device Dissipation @ $T_C = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	$P_D$	1.5 12	Watts mW/ $^\circ\text{C}$
Operating and Storage Junction Temperature Range	$T_J, T_{stg}$	-55 to +150	$^\circ\text{C}$

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

#### THERMAL CHARACTERISTICS (Note 1)

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	200	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	83.3	$^\circ\text{C}/\text{W}$

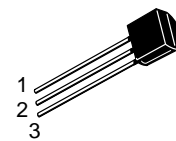
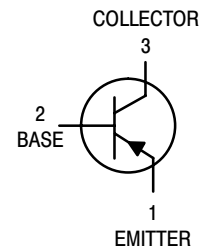
1. Indicates Data in addition to JEDEC Requirements.

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



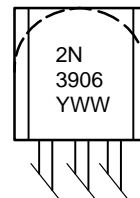
ON Semiconductor®

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TO-92  
CASE 29  
STYLE 1

#### MARKING DIAGRAMS



Y = Year  
WW = Work Week

#### ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

Preferred devices are recommended choices for future use and best overall value.

## 2N3906

### ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
<b>OFF CHARACTERISTICS</b>				
Collector–Emitter Breakdown Voltage (Note 2) (I <sub>C</sub> = 1.0 mAdc, I <sub>B</sub> = 0)	V <sub>(BR)CEO</sub>	40	–	Vdc
Collector–Base Breakdown Voltage (I <sub>C</sub> = 10 μAdc, I <sub>E</sub> = 0)	V <sub>(BR)CBO</sub>	40	–	Vdc
Emitter–Base Breakdown Voltage (I <sub>E</sub> = 10 μAdc, I <sub>C</sub> = 0)	V <sub>(BR)EBO</sub>	5.0	–	Vdc
Base Cutoff Current (V <sub>CE</sub> = 30 Vdc, V <sub>EB</sub> = 3.0 Vdc)	I <sub>BL</sub>	–	50	nAdc
Collector Cutoff Current (V <sub>CE</sub> = 30 Vdc, V <sub>EB</sub> = 3.0 Vdc)	I <sub>CEx</sub>	–	50	nAdc

### ON CHARACTERISTICS (Note 2)

DC Current Gain (I <sub>C</sub> = 0.1 mAdc, V <sub>CE</sub> = 1.0 Vdc) (I <sub>C</sub> = 1.0 mAdc, V <sub>CE</sub> = 1.0 Vdc) (I <sub>C</sub> = 10 mAdc, V <sub>CE</sub> = 1.0 Vdc) (I <sub>C</sub> = 50 mAdc, V <sub>CE</sub> = 1.0 Vdc) (I <sub>C</sub> = 100 mAdc, V <sub>CE</sub> = 1.0 Vdc)	h <sub>FE</sub>	60 80 100 60 30	– – 300 – –	–
Collector–Emitter Saturation Voltage (I <sub>C</sub> = 10 mAdc, I <sub>B</sub> = 1.0 mAdc) (I <sub>C</sub> = 50 mAdc, I <sub>B</sub> = 5.0 mAdc)	V <sub>CE(sat)</sub>	– –	0.25 0.4	Vdc
Base–Emitter Saturation Voltage (I <sub>C</sub> = 10 mAdc, I <sub>B</sub> = 1.0 mAdc) (I <sub>C</sub> = 50 mAdc, I <sub>B</sub> = 5.0 mAdc)	V <sub>BE(sat)</sub>	0.65 –	0.85 0.95	Vdc

### SMALL–SIGNAL CHARACTERISTICS

Current–Gain – Bandwidth Product (I <sub>C</sub> = 10 mAdc, V <sub>CE</sub> = 20 Vdc, f = 100 MHz)	f <sub>T</sub>	250	–	MHz
Output Capacitance (V <sub>CB</sub> = 5.0 Vdc, I <sub>E</sub> = 0, f = 1.0 MHz)	C <sub>obo</sub>	–	4.5	pF
Input Capacitance (V <sub>EB</sub> = 0.5 Vdc, I <sub>C</sub> = 0, f = 1.0 MHz)	C <sub>ibo</sub>	–	10	pF
Input Impedance (I <sub>C</sub> = 1.0 mAdc, V <sub>CE</sub> = 10 Vdc, f = 1.0 kHz)	h <sub>ie</sub>	2.0	12	kΩ
Voltage Feedback Ratio (I <sub>C</sub> = 1.0 mAdc, V <sub>CE</sub> = 10 Vdc, f = 1.0 kHz)	h <sub>re</sub>	0.1	10	X 10 <sup>–4</sup>
Small–Signal Current Gain (I <sub>C</sub> = 1.0 mAdc, V <sub>CE</sub> = 10 Vdc, f = 1.0 kHz)	h <sub>fe</sub>	100	400	–
Output Admittance (I <sub>C</sub> = 1.0 mAdc, V <sub>CE</sub> = 10 Vdc, f = 1.0 kHz)	h <sub>oe</sub>	3.0	60	μmhos
Noise Figure (I <sub>C</sub> = 100 μAdc, V <sub>CE</sub> = 5.0 Vdc, R <sub>S</sub> = 1.0 kΩ, f = 1.0 kHz)	NF	–	4.0	dB

### SWITCHING CHARACTERISTICS

Delay Time	(V <sub>CC</sub> = 3.0 Vdc, V <sub>BE</sub> = 0.5 Vdc, I <sub>C</sub> = 10 mAdc, I <sub>B1</sub> = 1.0 mAdc)	t <sub>d</sub>	–	35	ns
Rise Time		t <sub>r</sub>	–	35	ns
Storage Time	(V <sub>CC</sub> = 3.0 Vdc, I <sub>C</sub> = 10 mAdc, I <sub>B1</sub> = I <sub>B2</sub> = 1.0 mAdc)	t <sub>s</sub>	–	225	ns
Fall Time	(V <sub>CC</sub> = 3.0 Vdc, I <sub>C</sub> = 10 mAdc, I <sub>B1</sub> = I <sub>B2</sub> = 1.0 mAdc)	t <sub>f</sub>	–	75	ns

2. Pulse Test: Pulse Width ≤ 300 μs; Duty Cycle ≤ 2%.

### ORDERING INFORMATION

Device	Package	Shipping†
2N3906	TO–92	5,000 Units / Box
2N3906G	TO–92 (Pb–Free)	5,000 Units / Box
2N3906RL1	TO–92	5,000 Units / Box
2N3906RLRA	TO–92	2,000 / Tape & Reel
2N3906RLRAG	TO–92 (Pb–Free)	2,000 / Tape & Reel
2N3906RLRM	TO–92	2,000 / Ammo Pack
2N3906RLRMG	TO–92 (Pb–Free)	2,000 / Ammo Pack
2N3906RLRP	TO–92	2,000 / Tape & Reel
2N3906ZL1	TO–92	2,000 / Ammo Pack

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

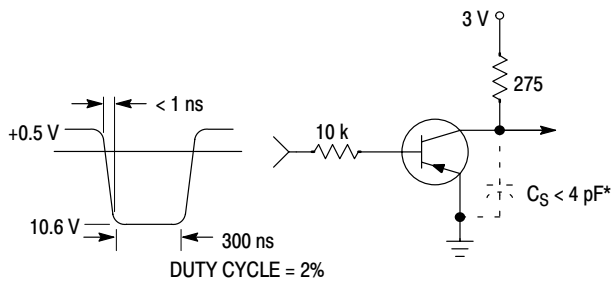


Figure 1. Delay and Rise Time Equivalent Test Circuit

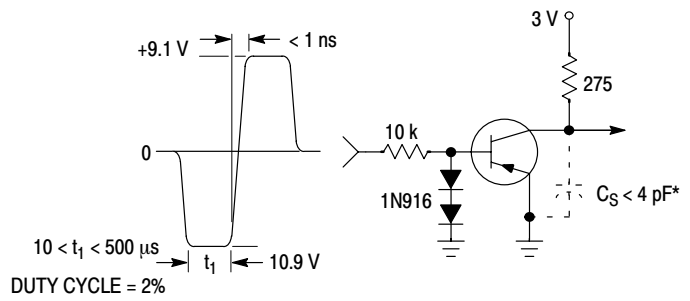


Figure 2. Storage and Fall Time Equivalent Test Circuit

\* Total shunt capacitance of test jig and connectors

TYPICAL TRANSIENT CHARACTERISTICS

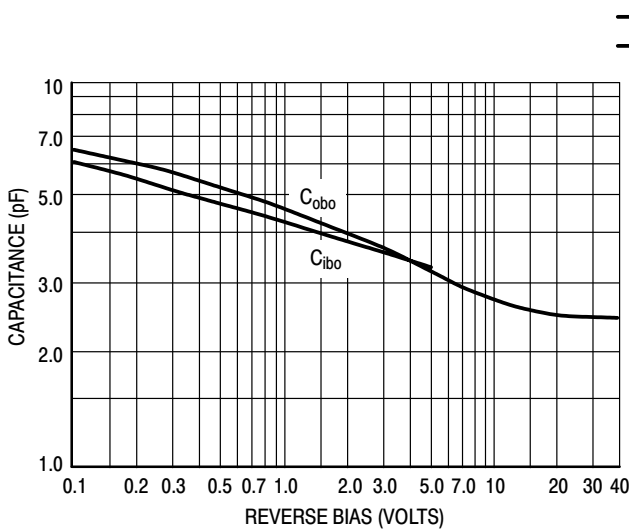


Figure 3. Capacitance

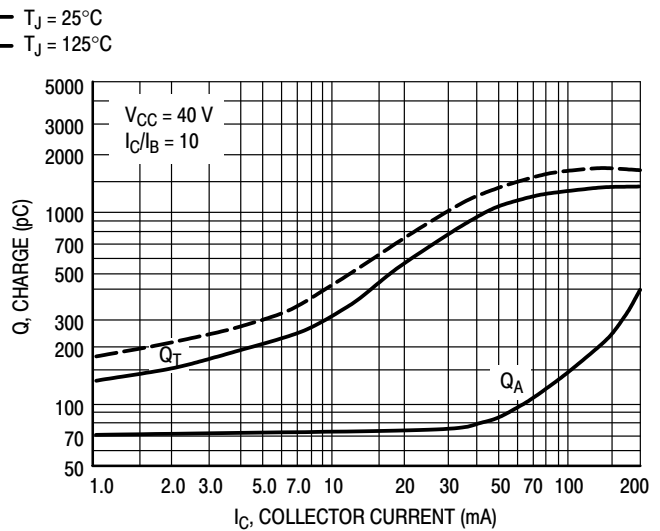


Figure 4. Charge Data

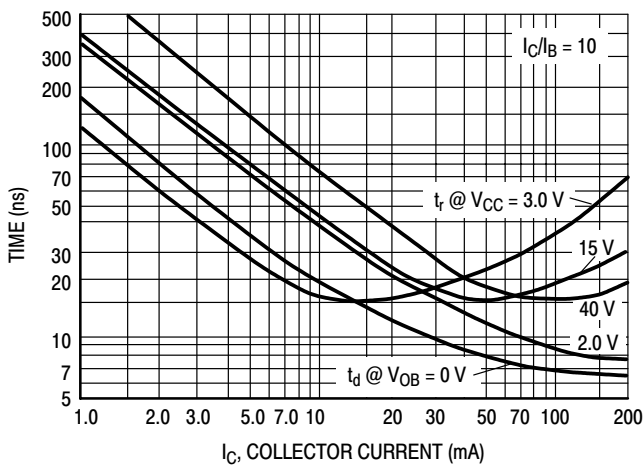


Figure 5. Turn-On Time

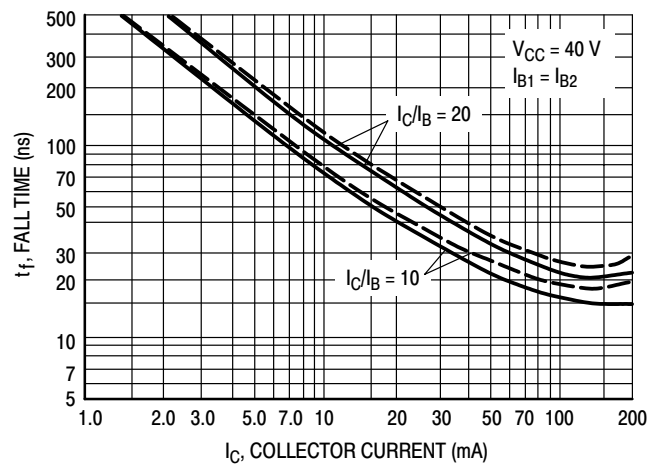


Figure 6. Fall Time

**TYPICAL AUDIO SMALL-SIGNAL CHARACTERISTICS**  
**NOISE FIGURE VARIATIONS**

( $V_{CE} = -5.0$  Vdc,  $T_A = 25^\circ\text{C}$ , Bandwidth = 1.0 Hz)

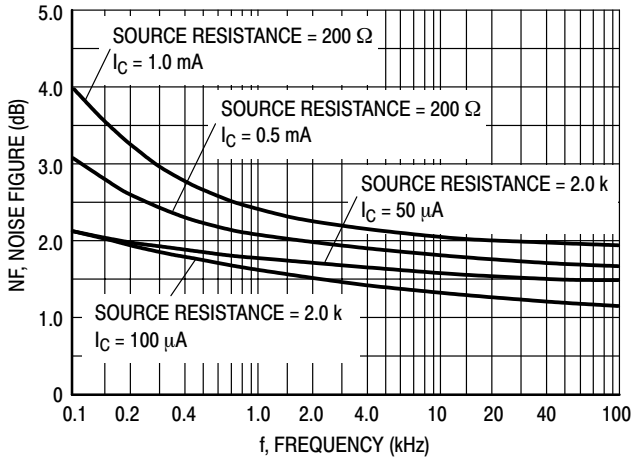


Figure 7.

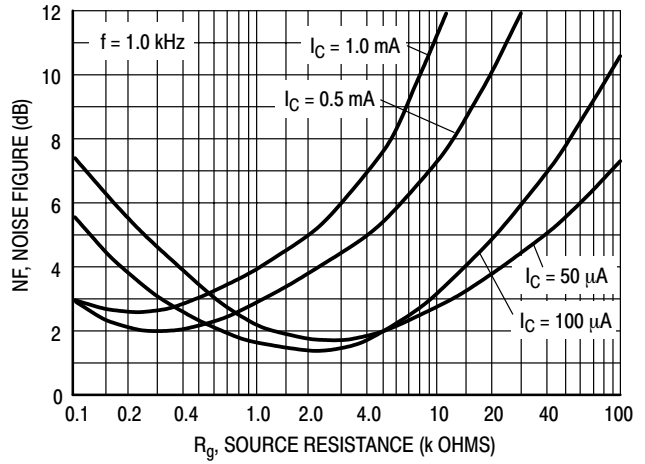


Figure 8.

**h PARAMETERS**

( $V_{CE} = -10$  Vdc,  $f = 1.0$  kHz,  $T_A = 25^\circ\text{C}$ )

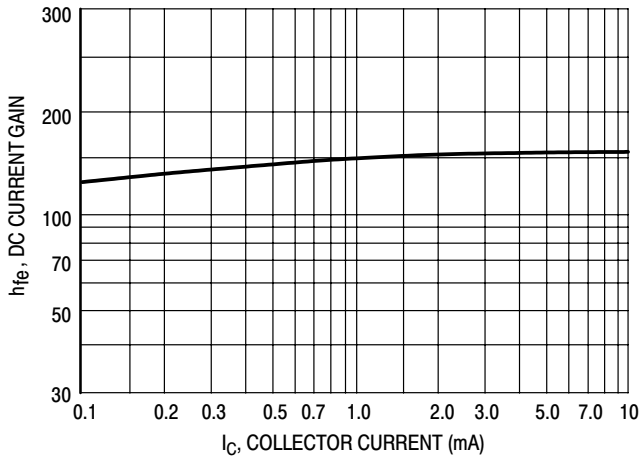


Figure 9. Current Gain

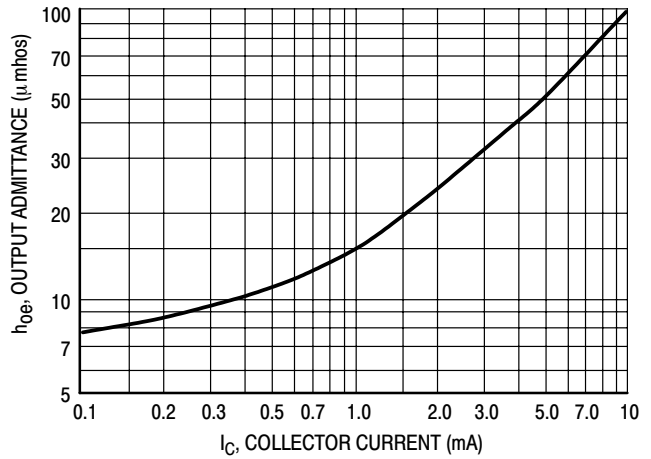


Figure 10. Output Admittance

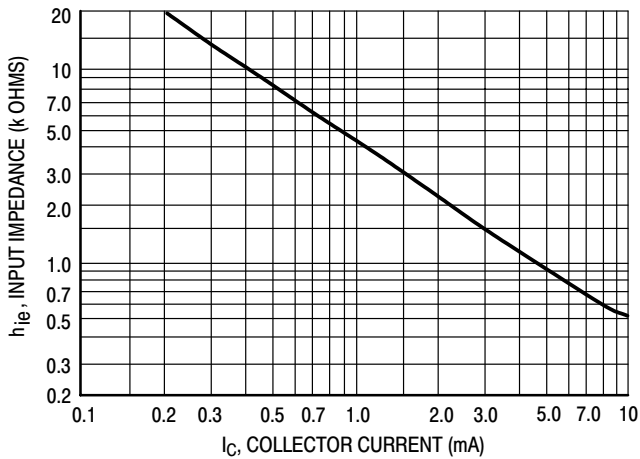


Figure 11. Input Impedance

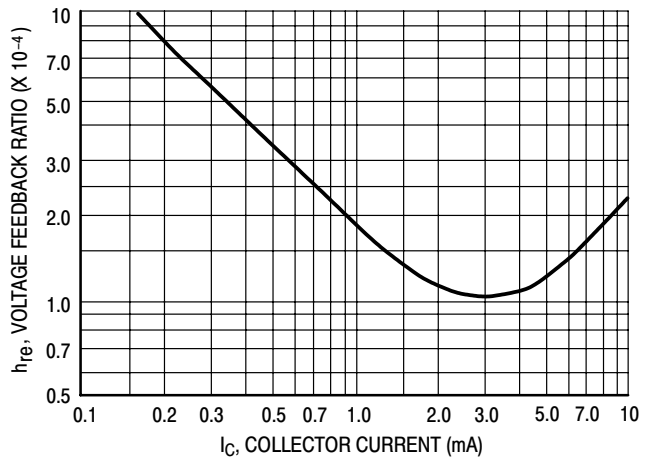


Figure 12. Voltage Feedback Ratio

TYPICAL STATIC CHARACTERISTICS

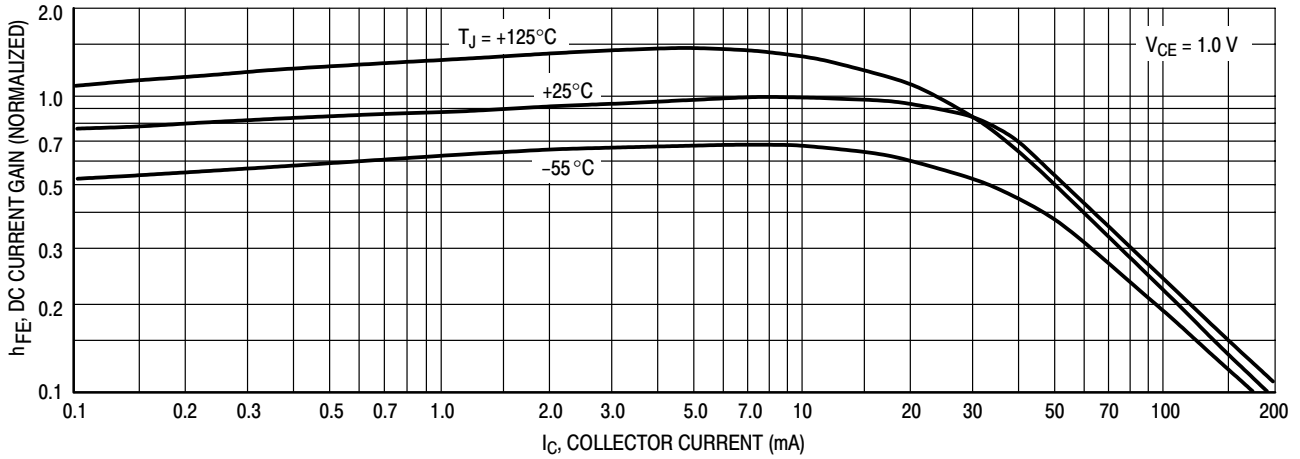


Figure 13. DC Current Gain

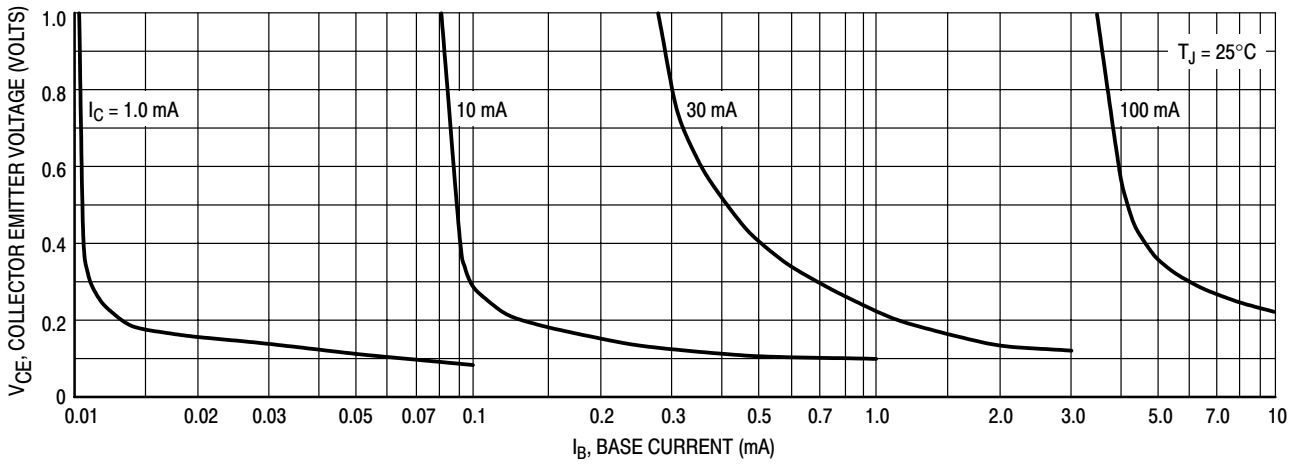


Figure 14. Collector Saturation Region

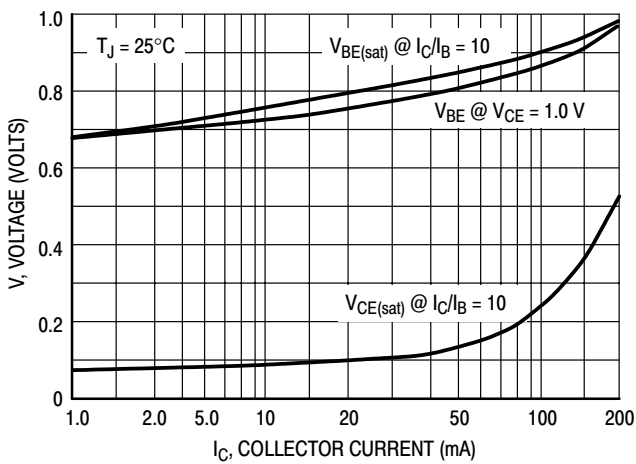


Figure 15. "ON" Voltages

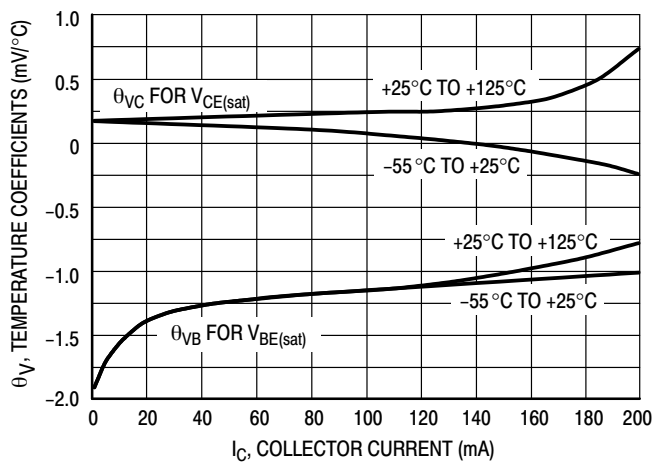
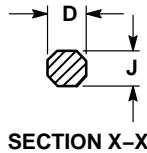
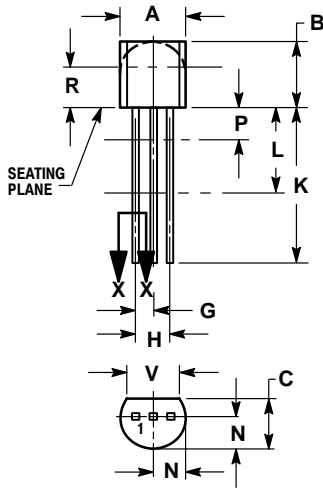


Figure 16. Temperature Coefficients

# 2N3906

## PACKAGE DIMENSIONS

TO-92  
TO-226AA  
CASE 29-11  
ISSUE AL



**NOTES:**

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
4. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.175	0.205	4.45	5.20
B	0.170	0.210	4.32	5.33
C	0.125	0.165	3.18	4.19
D	0.016	0.021	0.407	0.533
G	0.045	0.055	1.15	1.39
H	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500	---	12.70	---
L	0.250	---	6.35	---
N	0.080	0.105	2.04	2.66
P	---	0.100	---	2.54
R	0.115	---	2.93	---
V	0.135	---	3.43	---

**STYLE 1:**

1. EMITTER
2. BASE
3. COLLECTOR

**STYLE 14:**

1. EMITTER
2. COLLECTOR
3. BASE

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