



1N5391G THRU 1N5399G

1.5 AMPS. Glass Passivated Rectifiers



Voltage Range
50 to 1000 Volts
Current
1.5 Amperes

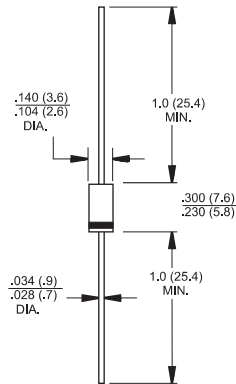
Features

- ✧ Low forward voltage drop
- ✧ High current capability
- ✧ High reliability
- ✧ High surge current capability

Mechanical Data

- ✧ Cases: Molded plastic
- ✧ Epoxy: UL 94V-0 rate flame retardant
- ✧ Lead: Axial leads, solderable per MIL-STD-202, Method 208 guaranteed
- ✧ Polarity: Color band denotes cathode end
- ✧ High temperature soldering guaranteed: 260°C/10 seconds/.375", (9.5mm) lead lengths at 5 lbs., (2.3kg) tension
- ✧ Weight: 0.40 gram

DO-15



Dimensions in inches and (millimeters)

Maximum Ratings and Electrical Characteristics

Rating at 25°C ambient temperature unless otherwise specified.
Single phase, half wave, 60 Hz, resistive or inductive load.
For capacitive load, derate current by 20%

Type Number	Symbol	1N	1N	1N	1N	1N	1N	1N	Units
		5391G	5392G	5393G	5395G	5397G	5398G	5399G	
Maximum Recurrent Peak Reverse Voltage	V_{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS Voltage	V_{RMS}	35	70	140	280	420	560	700	V
Maximum DC Blocking Voltage	V_{DC}	50	100	200	400	600	800	1000	V
Maximum Average Forward Rectified Current .375"(9.5mm) Lead Length @ $T_A = 60^\circ C$	$I_{(AV)}$	1.5							A
Peak Forward Surge Current, 8.3 ms Single Half Sine-wave Superimposed on Rated Load (JEDEC method)	I_{FSM}	50							A
Maximum Instantaneous Forward Voltage @ 1.5A	V_F	1.1		1.0					V
Maximum DC Reverse Current @ $T_A=25^\circ C$ at Rated DC Blocking Voltage @ $T_A=125^\circ C$	I_R	5.0 100							μA μA
Typical Junction Capacitance (Note 1)	C_j	15							pF
Typical Thermal Resistance (Note 2)	$R_{\theta JA}$	65							$^\circ C/W$
Operating and Storage Temperature Range	T_J, T_{STG}	- 65 to + 150							$^\circ C$

Note: 1. Measured at 1 MHz and Applied Reverse Voltage of 4.0 Volts D.C.
2. Mount on Cu-Pad Size 10mm x 10mm on P.C.B.

RATINGS AND CHARACTERISTIC CURVES (1N5391G THRU 1N5399G)

FIG.1- TYPICAL FORWARD CHARACTERISTICS

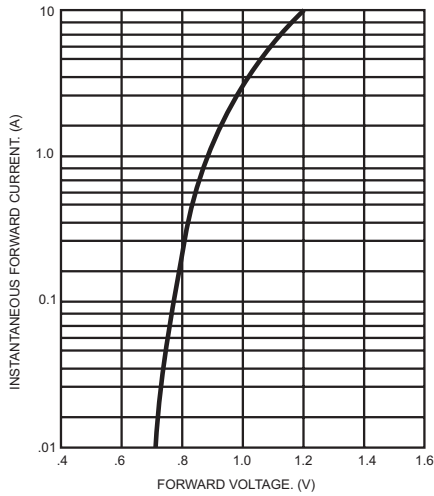


FIG.2- MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

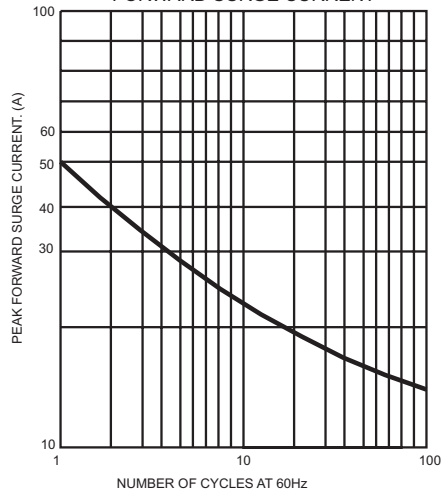


FIG.3- MAXIMUM FORWARD CURRENT DERATING CURVE

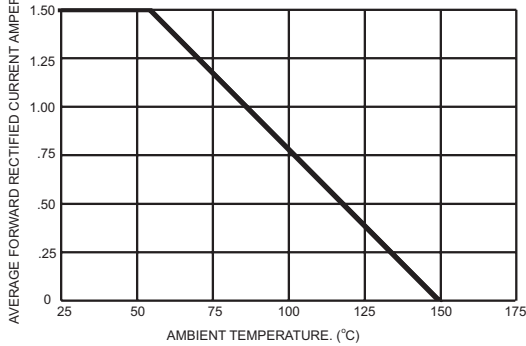
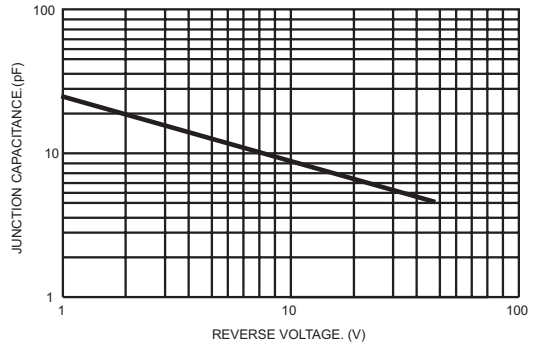


FIG.4- TYPICAL JUNCTION CAPACITANCE





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