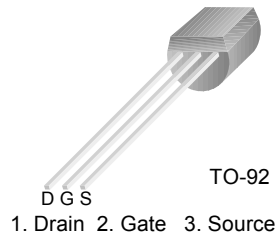


# BF246B

## N-Channel Switch

- This device is designed for low level analog switching, sample and hold circuits and chopper stabilized amplifiers.
- Sourced from process 51.
- See J111 for characteristics.



### Absolute Maximum Ratings\* T<sub>a</sub> = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
V <sub>DG</sub>	Drain-Gate Voltage	25	V
V <sub>GS</sub>	Gate-Source Voltage	-25	V
I <sub>GF</sub>	Forward Gate Current	50	mA
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range	-55 ~ 150	°C

\* These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

**Notes:**

1. These ratings are based on a maximum junction temperature of 150 degrees C.
2. These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

### Electrical Characteristics T<sub>a</sub> = 25°C unless otherwise noted

Symbol	Parameter	Conditions	Min.	Max	Units
<b>Off Characteristics</b>					
V <sub>(BR)GSS</sub>	Gate-Source Breakdown Voltage	I <sub>G</sub> = 1.0μA, V <sub>DS</sub> = 0	-25		V
I <sub>GSS</sub>	Gate Reverse Current	V <sub>GS</sub> = -15V, V <sub>DS</sub> = 0		-5.0	nA
V <sub>GS(off)</sub>	Gate-Source Cutoff Voltage	V <sub>DS</sub> = 15V, I <sub>D</sub> = 10nA	-0.6	-14.5	V
<b>On Characteristics*</b>					
I <sub>DSS</sub>	Zero-Gate Voltage Drain Current *	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 0	60	140	mA

### Thermal Characteristics T<sub>a</sub> = 25°C unless otherwise noted

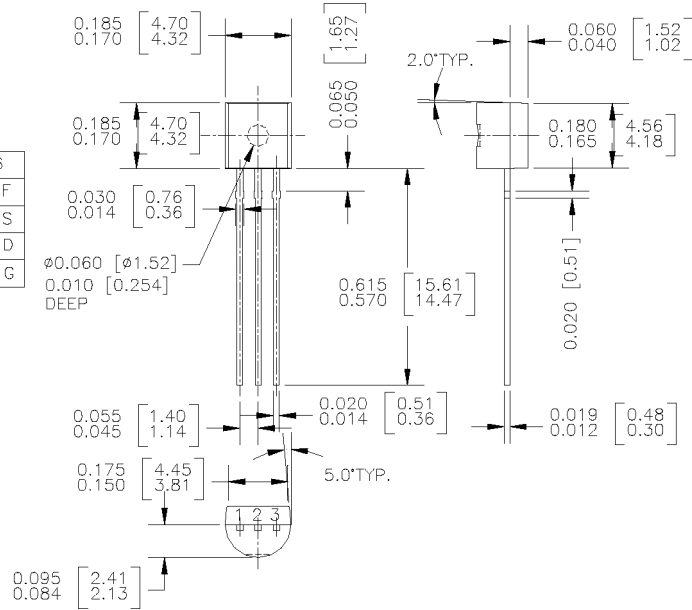
Symbol	Parameter	Value	Units
P <sub>D</sub>	Total Device Dissipation	625 5.0	mW mW/°C
R <sub>θJC</sub>	Thermal Resistance, Junction to Case	125	°C/W
R <sub>θJA</sub>	Thermal Resistance, Junction to Ambient	357	°C/W

Mechanical Dimensions

TO-92

TO-92 (92,94,96)

PIN	92		94		96	
	B	F	B	F	B	F
1	E	D	E	D	B	S
2	B	S	C	G	E	D
3	C	G	B	S	C	G



Dimensions in Millimeters

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E <sup>2</sup> CMOS™	I <sup>2</sup> C™	MSX™	QT Optoelectronics™	TinyLogic®
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### Definition of Terms

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