

T-37-21

PN Unijunction Transistors

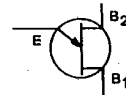
Silicon PN Unijunction Transistors

... designed for military and industrial use in pulse, timing, triggering, sensing, and oscillator circuits. The annular process provides low leakage current, fast switching and low peak-point currents as well as outstanding reliability and uniformity. Recommended usage includes:

- Silicon Controlled Rectifier Triggering Circuits — 2N4948
- Long-time Delay Circuits — 2N4949

2N4948
2N4949

PN UJT's



CASE 22A-01
STYLE 1

3

MAXIMUM RATINGS (T_A = 25°C unless otherwise noted.)

Rating	Symbol	Value	Unit
RMS Power Dissipation, Note 1	P _D	360	mW
RMS Emitter Current	I _e	50	mA
Peak Pulse Emitter Current, Note 2	I _e	1	Amp
Emitter Reverse Voltage	V _{B2E}	30	Volts
Storage Temperature Range	T _{stg}	-65 to +200	°C

Notes: 1. Derate 2.4 mW/°C increase in ambient temperature. Total power dissipation (available power to Emitter and Base-Two) must be limited by the external circuitry. Interbase voltage (V_{B2B1}) limited by power dissipation, V_{B2B1} = $\sqrt{R_{BB} \cdot P_D}$.

2. Capacitance discharge current must fall to 0.37 Amp within 3 ms and PRR ≤ 10 PPS.

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ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted.)

Characteristic	Symbol	Min	Typ	Max	Unit
Intrinsic Standoff Ratio (V _{B2B1} = 10 V), Note 1	η	0.55 0.74	—	0.82 0.86	—
Interbase Resistance (V _{B2B1} = 3 V, I _E = 0)	R _{BB}	4	7	12.0	k ohms
Interbase Resistance Temperature Coefficient (V _{B2B1} = 3 V, I _E = 0, T _A = -65°C to +100°C)	αR _{BB}	0.1	—	0.9	%/°C
Emitter Saturation Voltage (V _{B2B1} = 10 V, I _E = 50 mA), Note 2	V _{EB1(sat)}	—	2.5	3	Volts
Modulated Interbase Current (V _{B2B1} = 10 V, I _E = 50 mA)	I _{B2(mod)}	12	15	—	mA
Emitter Reverse Current (V _{B2E} = 30 V, I _{B1} = 0) (V _{B2E} = 30 V, I _{B1} = 0, T _A = 125°C)	I _{EB2O}	—	5	10	nA μA
Peak Point Emitter Current (V _{B2B1} = 25 V)	I _p	—	0.6	2	μA
Valley Point Current (V _{B2B1} = 20 V, R _{B2} = 100 ohms), Note 2	I _v	2	4	—	mA
Base-One Peak Pulse Voltage (Note 3, Figure 3)	V _{OBS1}	3 6	5 8	—	Volts
Maximum Oscillation Frequency (Figure 4)	f(max)	—	400	—	kHz

Notes:

1. Intrinsic standoff ratio, η, is defined by equation:

$$\eta = \frac{V_p - V_{EB1}}{V_{B2B1}}$$

Where V_p = Peak Point Emitter Voltage

V_{B2B1} = Interbase Voltage

V_F = Emitter to Base-One Junction Diode Drop
(≈ 0.45 V @ 10 μA)

2. Use pulse techniques: PW ≈ 300 μs duty cycle ≤ 2% to avoid internal heating due to interbase modulation which may result in erroneous readings.

3. Base-One Peak Pulse Voltage is measured in circuit of Figure 3. This specification is used to ensure minimum pulse amplitude for applications in SCR firing circuits and other types of pulse circuits.

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FIGURE 1 - UNIJUNCTION TRANSISTOR SYMBOL AND NOMENCLATURE

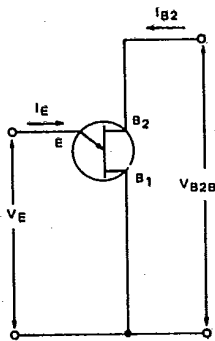


FIGURE 2 - STATIC EMITTER CHARACTERISTICS CURVES (Exaggerated to Show Details)

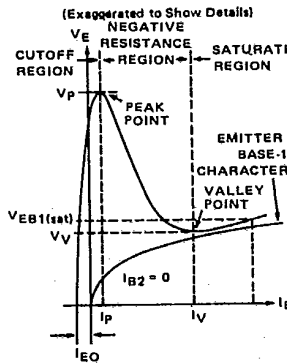


FIGURE 3 - V_{OBS1} TEST CIRCUIT (Typical Relaxation Oscillator)

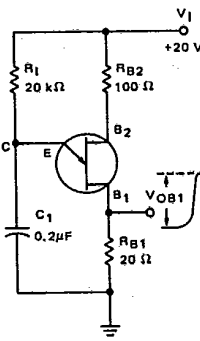
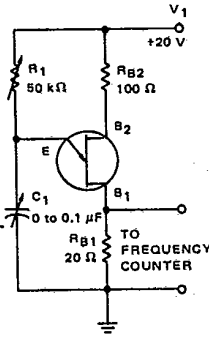


FIGURE 4 - F(max) MAXIMUM FREQUENCY TEST CIRCUIT





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