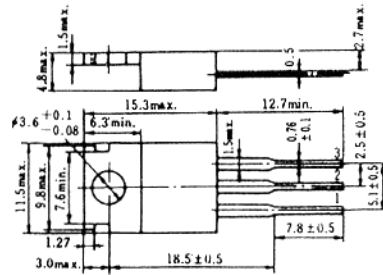


## 2SD1163, 2SD1163A

SILICON NPN TRIPLE DIFFUSED  
TV HORIZONTAL DEFLECTION OUTPUT



1. Emitter
  2. Collector  
(Flange)
  3. Base
- (Dimensions in mm)

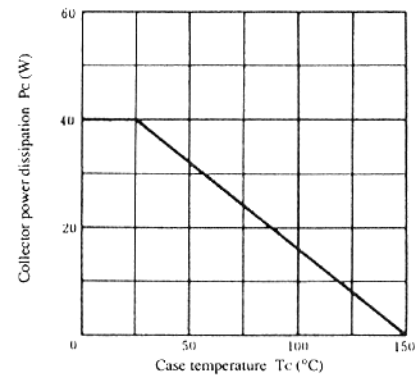
(JEDEC TO-220 AB)

### ■ ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

Item	Symbol	2SD1163	2SD1163A	Unit
Collector to base voltage	V <sub>CB0</sub>	300	350	V
Collector to emitter voltage	V <sub>CEO</sub>	120	150	V
Emitter to base voltage	V <sub>EBO</sub>	6	6	V
Collector current	I <sub>C</sub>	7	7	A
Collector peak current	i <sub>C(peak)</sub>	10	10	A
Collector surge current	i <sub>C(surge)</sub>	20	20	A
Collector power dissipation	P <sub>C</sub> *	40	40	W
Junction temperature	T <sub>j</sub>	150	150	°C
Storage temperature	T <sub>stg</sub>	-55 to +150	-55 to +150	°C

\* Value at T<sub>C</sub> = 25°C.

### MAXIMUM COLLECTOR DISSIPATION CURVE



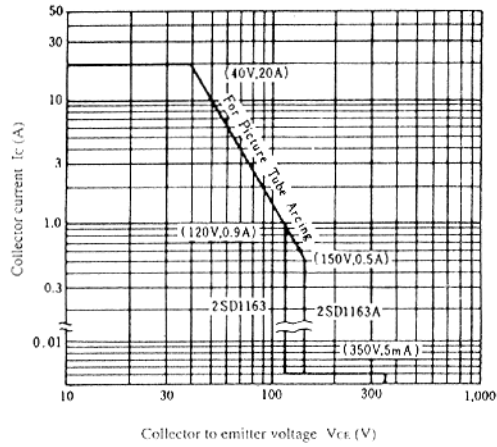
### ■ ELECTRICAL CHARACTERISTICS (Ta=25°C)

Item	Symbol	Test Condition	2SD1163			2SD1163A			Unit
			min.	typ.	max.	min.	typ.	max.	
Collector cutoff current	I <sub>CBO</sub>	V <sub>CB</sub> = 300V, I <sub>E</sub> = 0	—	—	5	—	—	—	mA
		V <sub>CB</sub> = 350V, I <sub>E</sub> = 0	—	—	—	—	—	5	mA
Collector to emitter breakdown voltage	V <sub>(BR)CEO</sub>	I <sub>C</sub> = 10mA, R <sub>BE</sub> = ∞	120	—	—	150	—	—	V
Emitter to base breakdown voltage	V <sub>(BR)EBO</sub>	I <sub>E</sub> = 10mA, I <sub>C</sub> = 0	6	—	—	6	—	—	V
DC current transfer ratio	h <sub>FE</sub>	V <sub>CE</sub> = 5V, I <sub>C</sub> = 5A*	25	—	—	25	—	—	
Collector to emitter saturation voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> = 5A, I <sub>B</sub> = 0.5A*	—	—	2.0	—	—	1.0	V
Base to emitter saturation voltage	V <sub>BE(sat)</sub>	I <sub>C</sub> = 5A, I <sub>B</sub> = 0.5A*	—	—	1.2	—	—	1.2	V
Fall time	t <sub>f</sub>	I <sub>CP</sub> = 3.5A, I <sub>B1</sub> = 0.45A	—	—	0.5	—	—	0.5	μs

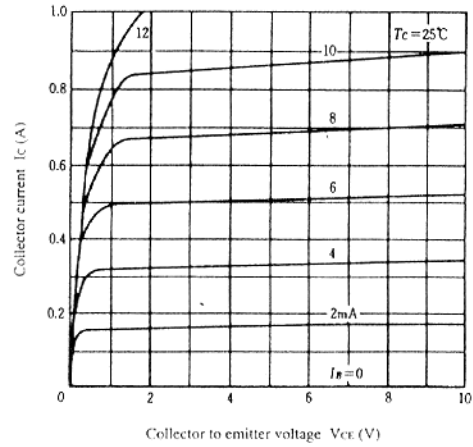
\* Pulse Test.

## 2SD1163, 2SD1163A

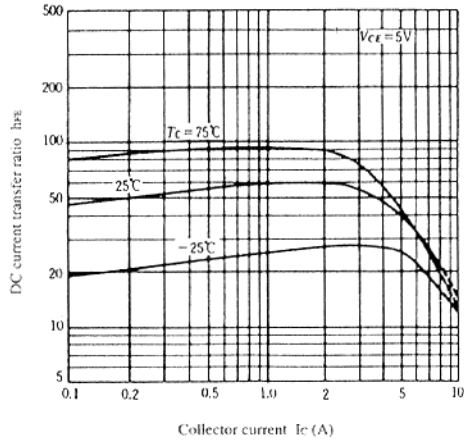
AREA OF SAFE OPERATION



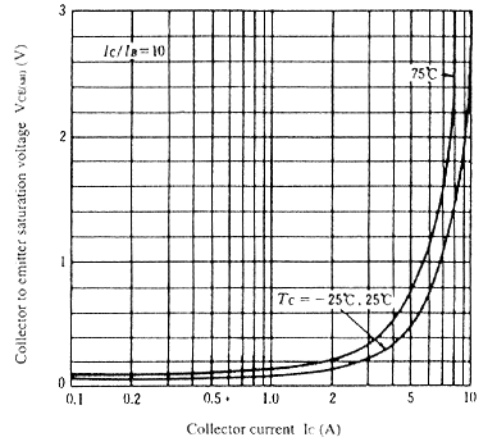
TYPICAL OUTPUT CHARACTERISTICS



DC CURRENT TRANSFER RATIO VS. COLLECTOR CURRENT



COLLECTOR TO EMITTER SATURATION VOLTAGE VS. COLLECTOR CURRENT



BASE TO EMITTER SATURATION VOLTAGE VS. COLLECTOR CURRENT

