

HFA15TB60

HEXFRED™

Ultrafast, Soft Recovery Diode

Features

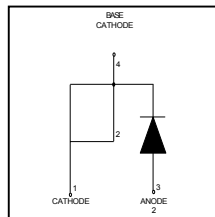
- Ultrafast Recovery
- Ultrasoft Recovery
- Very Low I_{RRM}
- Very Low Q_{rr}
- Specified at Operating Conditions

Benefits

- Reduced RFI and EMI
- Reduced Power Loss in Diode and Switching Transistor
- Higher Frequency Operation
- Reduced Snubbing
- Reduced Parts Count

Description

International Rectifier's HFA15TB60 is a state of the art ultra fast recovery diode. Employing the latest in epitaxial construction and advanced processing techniques it features a superb combination of characteristics which result in performance which is unsurpassed by any rectifier previously available. With basic ratings of 600 volts and 8 amps per Leg continuous current, the HFA15TB60 is especially well suited for use as the companion diode for IGBTs and MOSFETs. In addition to ultra fast recovery time, the HEXFRED product line features extremely low values of peak recovery current (I_{RRM}) and does not exhibit any tendency to "snap-off" during the t_b portion of recovery. The HEXFRED features combine to offer designers a rectifier with lower noise and significantly lower switching losses in both the diode and the switching transistor. These HEXFRED advantages can help to significantly reduce snubbing, component count and heatsink sizes. The HEXFRED HFA15TB60 is ideally suited for applications in power supplies and power conversion systems (such as inverters), motor drives, and many other similar applications where high speed, high efficiency is needed.



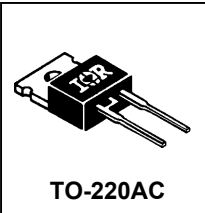
$$V_R = 600V$$

$$V_F = 1.7V$$

$$Q_{rr}^* = 84nC$$

$$di_{(rec)}/dt^* = 188A/\mu s$$

* 125°C



Absolute Maximum Ratings

	Parameter	Max	Units
V_R	Cathode-to-Anode Voltage	600	V
$I_F @ T_C = 100^\circ C$	Continuous Forward Current	15	A
I_{FSM}	Single Pulse Forward Current	150	
I_{FRM}	Maximum Repetitive Forward Current	60	
$P_D @ T_C = 25^\circ C$	Maximum Power Dissipation	74	W
$P_D @ T_C = 100^\circ C$	Maximum Power Dissipation	29	
T_J T_{STG}	Operating Junction and Storage Temperature Range	- 55 to +150	C

Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min	Typ	Max	Units	Test Conditions
V _{BR}	Cathode Anode Breakdown Voltage	600			V	I _R = 100μA
V _{FM}	Max Forward Voltage		1.3	1.7	V	I _F = 15A
			1.5	2.0		I _F = 30A See Fig. 1
			1.2	1.6		I _F = 15A, T _J = 125°C
I _{RM}	Max Reverse Leakage Current		1.0	10	μA	V _R = V _R Rated See Fig. 2
			400	1000		T _J = 125°C, V _R = 0.8 x V _R Rated
C _T	Junction Capacitance		25	50	pF	V _R = 200V See Fig. 3
L _S	Series Inductance		8.0		nH	Measured lead to lead 5mm from package body

Dynamic Recovery Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min	Typ	Max	Units	Test Conditions
t _{rr}	Reverse Recovery Time	19			ns	I _F = 1.0A, di/dt = 200A/μs, V _R = 30V T _J = 25°C T _J = 125°C
t _{rr1}	See Fig. 5, 6 & 16	42	60			
t _{rr2}		74	120			
I _{RRM1}	Peak Recovery Current	4.0	6.0		A	T _J = 25°C T _J = 125°C
I _{RRM2}	See Fig. 7 & 8	6.5	10			
Q _{rr1}	Reverse Recovery Charge	84	180		nC	T _J = 25°C T _J = 125°C
Q _{rr2}	See Fig. 9 & 10	241	600			
di _{(rec)M} /dt1	Peak Rate of Fall of Recovery Current	188			A/μs	T _J = 25°C T _J = 125°C
di _{(rec)M} /dt2	During t _b See Fig. 11 & 12	160				

Thermal - Mechanical Characteristics

	Parameter	Min	Typ	Max	Units
T _{lead} ①	Lead Temperature			300	°C
R _{thJC}	Thermal Resistance, Junction to Case			1.7	K/W
R _{thJA} ②	Thermal Resistance, Junction to Ambient			80	
R _{thCS} ③	Thermal Resistance, Case to Heat Sink		0.5		
Wt	Weight		2.0		g
			0.07		(oz)
T	Mounting Torque	6.0		12	Kg-cm
		5.0		10	lbf•in

① 0.063 in. from Case (1.6mm) for 10 sec

② Typical Socket Mount

③ Mounting Surface, Flat, Smooth and Greased

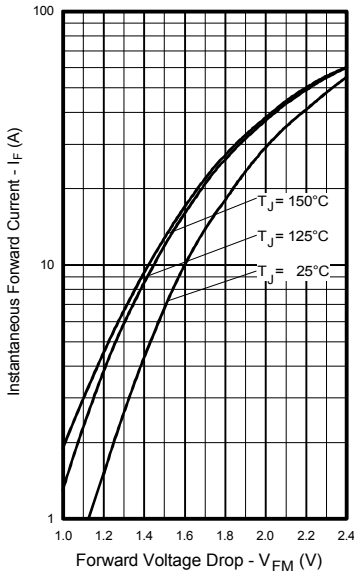


Fig. 1 - Maximum Forward Voltage Drop vs. Instantaneous Forward Current

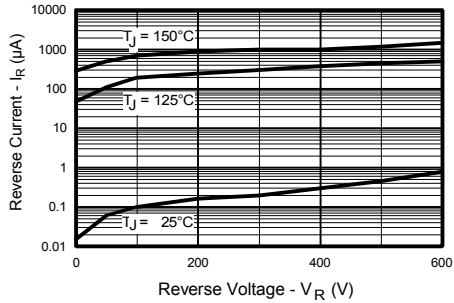


Fig. 2 - Typical Reverse Current vs. Reverse Voltage

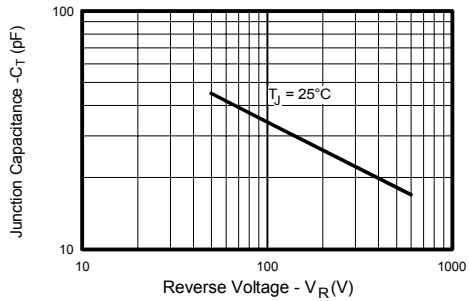


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

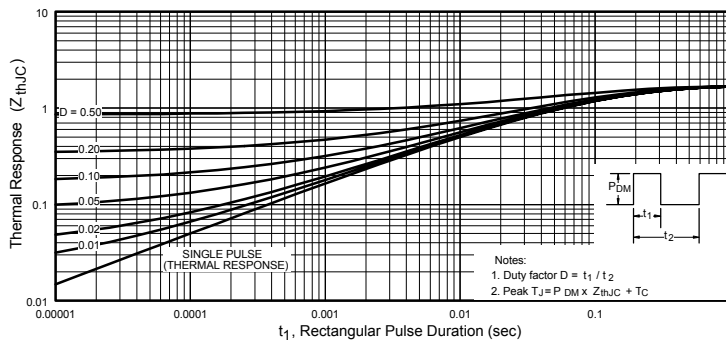


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

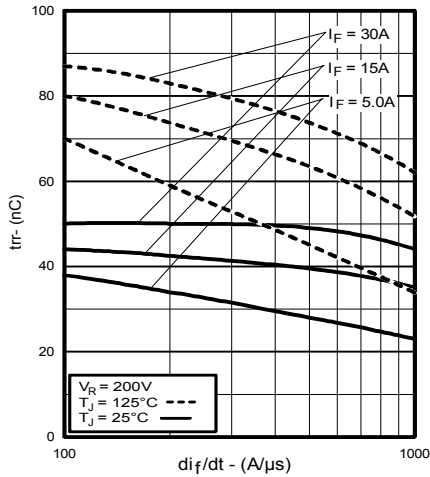


Fig. 5 - Typical Reverse Recovery vs. di_f/dt

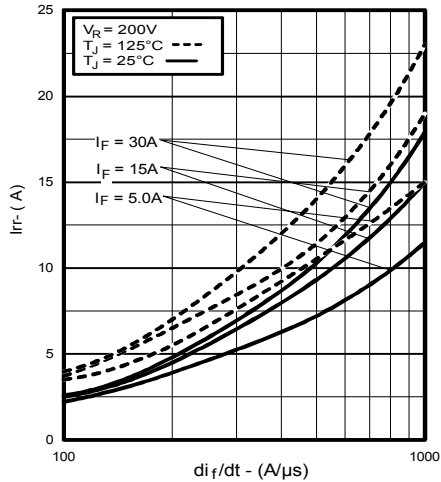


Fig. 6 - Typical Recovery Current vs. di_f/dt

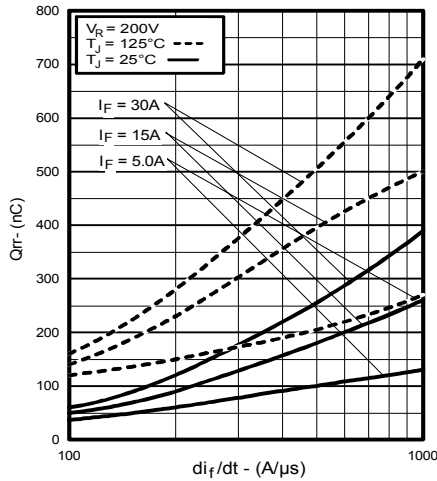


Fig. 7 - Typical Stored Charge vs. di_f/dt

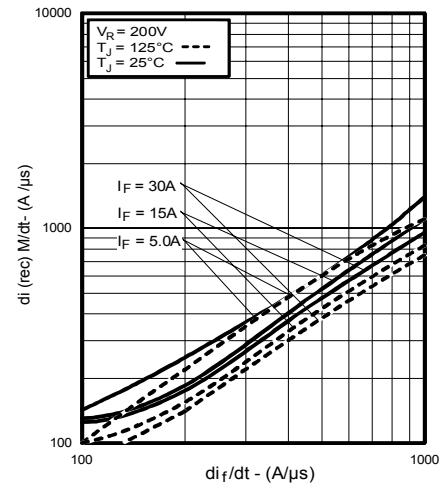


Fig. 8 - Typical $di_{(rec)M}/dt$ vs. di_f/dt

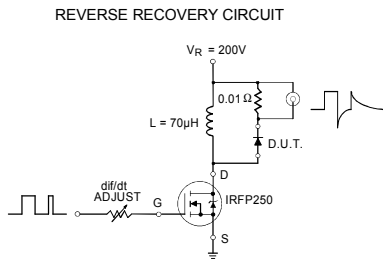


Fig. 9 - Reverse Recovery Parameter Test Circuit

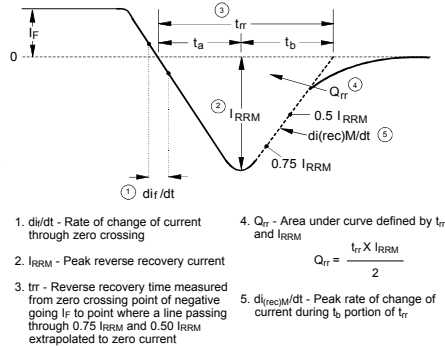
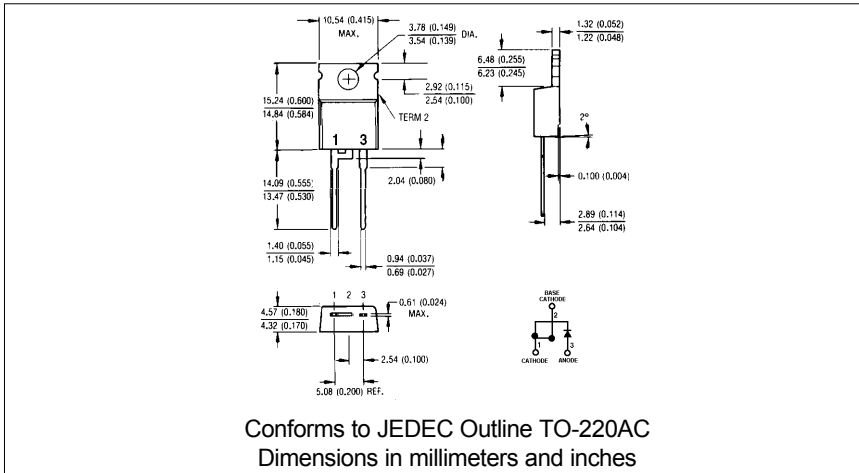


Fig. 10 - Reverse Recovery Waveform and Definitions

HFA15TB60

Bulletin PD-2.334 rev. A 11/00

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Data and specifications subject to change without notice.