No.940A

STK3062II

Thick Film Hybrid Integrated **Circuit**

2-CHANNEL **VOLTAGE AMPLIFIER FOR** 60 **TO** 70W **POWER AMPLIFIER**

Features

1. For AC amplifier circuit configuration, no external circuit for middle point adjustment is required.
2. High degree of freedom for phase compensation due to external parts.
3. Low distortion (0.01%) due to built-in current mirror circuit.
4. 2 channels, single package, and dual-supply operation.

Maximum Ratings at Ta=25°C

			unit
Maximum Supply Voltage	V_{CC}max	±59	V
Operating Case Temperature	T_C	100	°C
Allowable Power Dissipation	Pdmax Per channel	4	W
Storage Temperature	Tstg	-30 to +100	°C

Recommended Operating Conditions at Ta=25°C

			unit
Recommended Supply Voltage	V_{CC}	±41	V
Load Resistance	RL	33	kohm

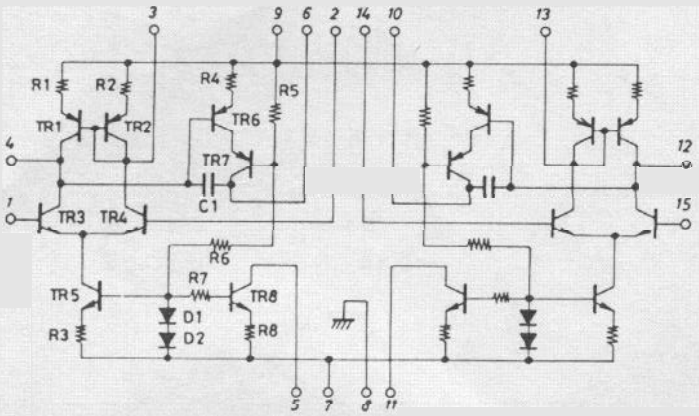
Operating Characteristics at Ta=25°C, R_L=33kohm, VG=40dB, at Test Circuit.

			min	typ	max	unit
Current Dissipation	I_{CC}	V_{CC}=±48V		22	35	mA
Output Center Voltage	$V_{N(1)}$	V_{CC}=±48V	-50		+50	mV
Output Center Voltage	$V_{N(2)}$	V_{CC}=±48V, VG=26.5dB, SW1 to 4 : a	-70		+70	mV
Output Noise Voltage	V_{NO}*	V_{CC}=±48V, Rg=0ohm			7.0	mV
Input Resistance	ri	V_{CC}=±48V, f=1kHz,	31k	33k		Ohm
Total Harmonic Distortion	THD	V_{CC}=±41V, f=20kHz, vo=22V		0.005	0.01	%

*: Frequency response of VTVM for test must be within ±3dB at 5Hz to 1MHz.

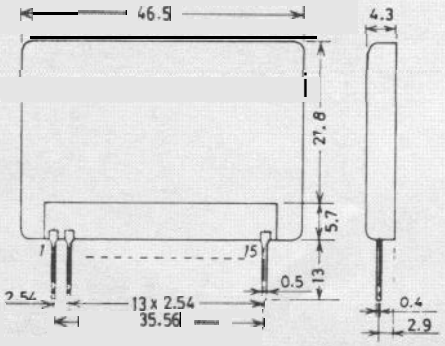
Note) For power supply for test, use constant voltage power supply unless otherwise specified.

Equivalent Circuit



Case Outline 4018

(unit:mm)



These specifications are subject to change without notice.

Test Circuit