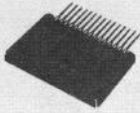


No. 940A

STK3062 II

**Thick Film Hybrid Integrated Circuit**  
**2-CHANNEL VOLTAGE AMPLIFIER FOR**  
**60 TO 70W POWER AMPLIFIER**

### Features

1. For AC amplifier circuit configuration, no external circuit for middle point adjustment is required.
2. High degree of freedom for phase compensation due to external parts.
3. Low distortion (**0.01%**) due to built-in current mirror circuit.
4. 2 channels, single package, and dual-supply operation.

### Maximum Ratings at $T_a=25^\circ\text{C}$

Parameter	Symbol	Value	Unit
Maximum Supply Voltage	$V_{CC\text{max}}$	$\pm 59$	V
Operating Case Temperature	$T_d$	100	$^\circ\text{C}$
Allowable Power Dissipation	$P_{d\text{max}}$	4	W
Storage Temperature	$T_{stg}$	-30 to +100	$^\circ\text{C}$

### Recommended Operating Conditions at $T_a=25^\circ\text{C}$

Parameter	Symbol	Value	Unit
Recommended Supply Voltage	$V_{CC}$	$\pm 41$	V
Load Resistance	$R_L$	33 kohm	

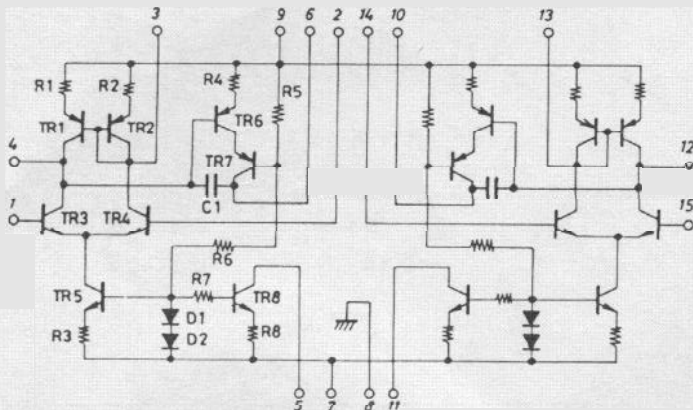
### Operating Characteristics at $T_a=25^\circ\text{C}$ , $R_L=33\text{kohm}$ , $V_G=40\text{dB}$ , at Test Circuit.

Parameter	Symbol	Conditions	min	typ	max	unit
Current Dissipation	$I_{CC}$	$V_{CC}=\pm 48\text{V}$		22	35	mA
Output Center Voltage	$V_{N(1)}$	$V_{CC}=\pm 48\text{V}$	-50		+50	mV
Output Center Voltage	$V_{N(2)}$	$V_{CC}=\pm 48\text{V}$ , $V_G=26.5\text{dB}$ , SW1 to 4 : a	-70		+70	mV
Output Noise Voltage	$V_{NO}^*$	$V_{CC}=\pm 48\text{V}$ , $R_g=0\text{ohm}$			7.0	mV
Input Resistance	$r_i$	$V_{CC}=\pm 48\text{V}$ , $f=1\text{kHz}$	31k	33k		Ohm
Total Harmonic Distortion	THD	$V_{CC}=\pm 41\text{V}$ , $f=20\text{kHz}$ , $V_O=22\text{V}$	0.005	0.01		%

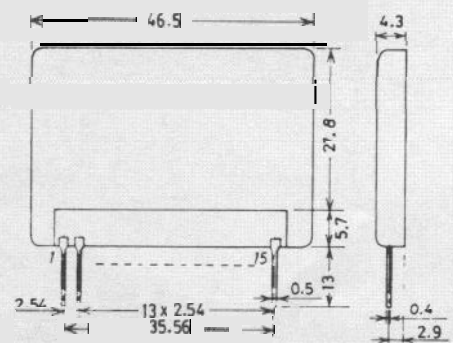
\*: Frequency response of VTVM for test must be within  $\pm 3\text{dB}$  at 5Hz to 1MHz.

Note) For power supply for test, use constant voltage power supply unless otherwise specified.

### Equivalent Circuit

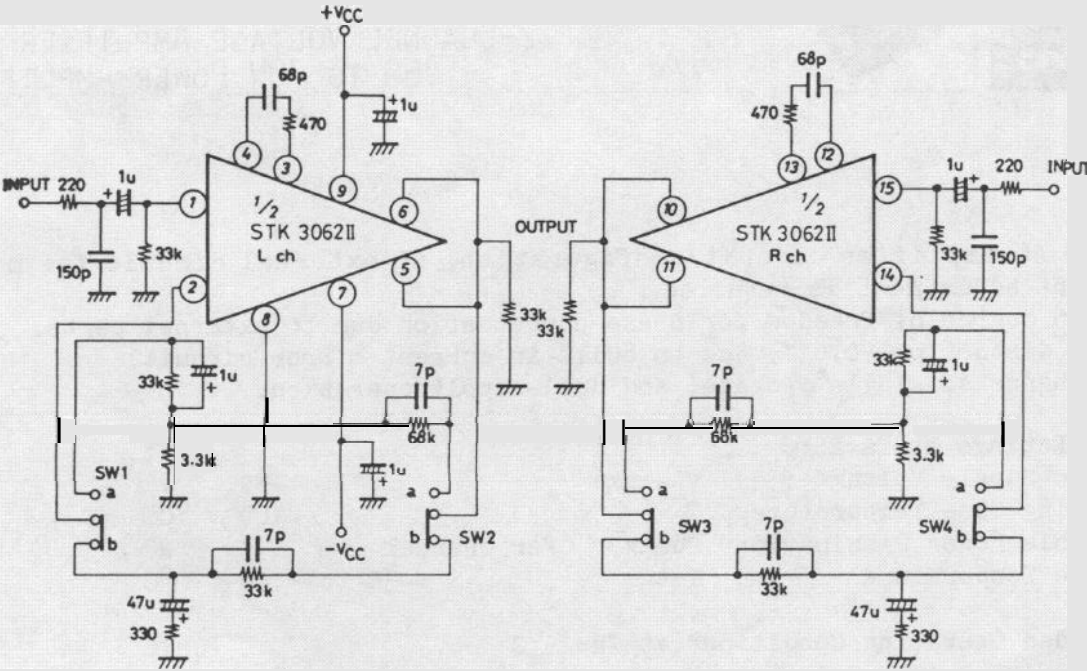


### Case Outline 4018 (unit:mm)



These specifications are subject to change without notice.

# Test Circuit



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